Formalization of Conflict Analysis of Programs with Procedures, Thread Creation, and Monitors in Isabelle/HOL

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Abstract
In this work we formally verify the soundness and precision of a static program analysis that detects conflicts (e.g. data races) in programs with procedures, thread creation and monitors with the Isabelle theorem prover. As common in static program analysis, our program model abstracts guarded branching by nondeterministic branching, but completely interprets the call-/return behavior of procedures, synchronization by monitors, and thread creation. The analysis is based on the observation that all conflicts already occur in a class of particularly restricted schedules. These restricted schedules are suited to constraint-system-based program analysis.

The formalization is based upon a flowgraph-based program model with an operational semantics as reference point.
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1 Introduction

Conflicts are a common programming error in parallel programs. A conflict occurs if the same resource is accessed simultaneously by more than one process. Given a program $\pi$ and two sets of control points $U$ and $V$, the analysis problem is to decide whether there is an execution of $\pi$ that simultaneously reaches one control point from $U$ and one from $V$.

In this work, we use a flowgraph-based program model that extends a previously studied model [6] by reentrant monitors. In our model, programs can call recursive procedures, dynamically create new threads and synchronize via reentrant monitors. As usual in static program analysis, our program model abstracts away guarded branching by nondeterministic choice. We use an operational semantics as reference point for the correctness proofs. It models parallel execution by interleaving, i.e. just one thread is executed at any time and context switches may occur after every step. The next step is nondeterministically selected from all threads ready for execution. The analysis is based on a constraint system generated from the flowgraph. From its least solution, one can decide whether control points from $U$ and $V$ are simultaneously reachable or not.

It is notoriously hard to analyze concurrent programs with constraint systems because of the arbitrary fine-grained interleaving. The key idea behind our analysis is to use a restricted scheduling: While the interleaving semantics can switch the context after each step, the restricted scheduling just allows context switches at certain points of a thread’s execution. We can show that each conflict is also reachable under this restricted scheduling. The restricted schedules can be easily analyzed with constraint systems as most of the complexity generated by arbitrary interleaving does no longer occur due to the restrictions. The remaining concurrency effects can be smoothly handled by using the concept of acquisition histories [5].

Related Work In [6] we present a constraint-system-based analysis for programs with thread creation and procedures but without monitors. The abstraction from synchronization is common in this line of research: There are automata-based techniques [1, 2, 3] as well as constraint-system-based techniques [7, 6] to analyze programs with procedures and either parallel calls or thread creation, but without any synchronization. In [5, 4] analysis techniques for interprocedural parallel programs with a fixed number of initial threads and nested locks are presented. These nested locks are not syntactically bound to the program structure, but assumed to be well-nested, that is any unlock statement is required to release the lock that was acquired last by the thread. Moreover, there is no support for reentrant
locks\textsuperscript{1}. We use monitors instead of locks. Monitors are syntactically bound
to the program structure and thus well-nestedness is guaranteed statically.
Additionally we directly support reentrant monitors. Our model cannot
simulate well-nested locks where a lock statement and its corresponding un-
lock statement may be in different procedures (as in [5, 4]). As common
programming languages like Java also use reentrant monitors rather than
locks, we believe our model to be useful as well.

**Document structure**  This document contains a commented formaliza-
tion of these ideas as a collection of Isabelle/HOL theories. A more abstract
description is in preparation. This document starts with formalization mon-
itor consistent interleaving (Section 2) and acquisition histories (Section 3).
Labeled transition systems are formalized in Section 4, and Section 5 defines
the notion of interleaving semantics. Flowgraphs are defined in Section 6,
and Section 7 describes their operational semantics. Section 8 contains the
formalization of the restricted interleaving and Section 9 contains the con-
straint systems. Finally, the main result of this development – the correct-
ness of the constraint systems w.r.t. to the operational semantics – is briefly
stated in Section 10.

2 Monitor Consistent Interleaving

```isar
theory ConsInterleave
imports Interleave Misc
begin

The monitor consistent interleaving operator is defined on two lists of arbi-
trary elements, provided an abstraction function $\alpha$ that maps list elements
to pairs of sets of monitors is available. $\alpha \ e = (M, M')$ intuitively means
that step $e$ enters the monitors in $M$ and passes (enters and leaves) the
monitors in $M'$. The consistent interleaving describes all interleavings of
the two lists that are consistent w.r.t. the monitor usage.

2.1 Monitors of lists of monitor pairs

The following defines the set of all monitors that occur in a list of pairs of
monitors. This definition is used in the following context: $\text{mon-pl \ (map \ \alpha \ w)}$ is the set of monitors used by a word $w$ w.r.t. the abstraction $\alpha$

**definition**

\[
\text{mon-pl \ w} \equiv \text{foldl \ (op \ \cup) \ \{\} \ (map \ (\lambda e. \ \text{fst} \ e \cup \ \text{snd} \ e) \ w)}
\]

**lemma** $\text{mon-pl-empty \ [simp]}$: $\text{mon-pl} \ [\ [] = \ \{\}$

\textsuperscript{1}Reentrant locks can always be simulated by non-reentrant ones, at the cost of a worst-case exponential blowup of the program size
lemma mon-pl-cons [simp]: mon-pl (e@w) = fst e ∪ snd e ⋃ mon-pl w

lemma mon-pl-unconc: !!b. mon-pl (a@b) = mon-pl a ⋃ mon-pl b

lemma mon-pl-ileq: w ≤ w' ⇒ mon-pl w ⋆ mon-pl w'

lemma mon-pl-set: mon-pl w = \bigcup \{ \{ e, e \in set w \} | e \}

fun cil :: 'a list ⇒ ('m set ⨿ 'm set) ⇒ 'a list
  (- ⨿ - [64,64,64] 64) where
  | w ⨿ α [] = \{ w \}
  | w ⨿ α [] = \{ w \}
— Interleaving with the empty word results in the empty word
  | e1#w1 ⨿ α e2#w2 = (if fst (α e1) ∩ mon-pl (map α (e2#w2))) = {} then
    e1#(w1 ⨿ α e2#w2)
    else {}
  ) ⋃ (if fst (α e2) ∩ mon-pl (map α (e1#w1))) = {} then
    e2#(e1#w1 ⨿ α w2)
    else {}
  )
— If both words are not empty, we can take the first step of one word, interleave
  the rest with the other word and then append the first step to all result set elements,
  provided it does not allocate a monitor that is used by the other word
Note that this definition allows reentrant monitors, because it only checks
that a monitor that is going to be entered by one word is not used in the
other word. Thus the same word may enter the same monitor multiple times.

The next lemmas are some auxiliary lemmas to simplify the handling of the
consistent interleaving operator.

lemma cil-last-case-split [cases set, case-names left right]:
  | w∈e1#w1 @α e2#w2;
  |!!w', |w=e1#w'; w'∈(w1 @α e2#w2);
  |         | fst (α e1) ∩ mon-pl (map α (e2#w2)) = {} ] ⇒ P;
  |!!w', |w=e2#w'; w'∈(e1#w1 @α w2);
  |         | fst (α e2) ∩ mon-pl (map α (e1#w1)) = {} ] ⇒ P
  ] ⇒ P

lemma cil-cases [cases set, case-names both-empty left-empty right-empty app-left app-right]:
lemma cil-induct\*[case-names both-empty left-empty right-empty append]: []
\[\alpha. \ P \ α \ []:;\]
\[\alpha \ ad \ ae. \ P \ α \ [] (ad \ # \ ae);\]
\[\alpha \ z \ aa. \ P \ α (z \ # \ aa) [];;\]
\[\alpha \ e1 \ w1 \ e2 \ w2. \]
\[\text{fst} (\alpha \ e1) \cap \text{mon-pl} (\text{map} \ α (e2 \ # \ w2)) = {} \implies P \ α w1 (e2 \ # \ w2);\]
\[\text{fst} (\alpha \ e2) \cap \text{mon-pl} (\text{map} \ α (e1 \ # \ w1)) = {} \implies P \ α (e1 \ # \ w1) w2\]
\[\implies P \ α (e1 \ # \ w1) (e2 \ # \ w2)\]
\[\implies P \ α w \ w\]
\[\langle \text{proof} \rangle\]

lemma cil-induct-fixα\*[case-names both-empty left-empty right-empty append]: []
\[\alpha. \ P \ α \ []:;\]
\[\alpha \ ad \ ae. \ P \ α \ [] (ad \ # \ ae);\]
\[\alpha \ z \ aa. \ P \ α (z \ # \ aa) [];;\]
\[\alpha \ e 1 \ w1 \ e2 \ w2. \]
\[\text{fst} (\alpha \ e2) \cap \text{mon-pl} (\text{map} \ α (e1 \ # \ w1)) = {} \implies P \ α (e1 \ # \ w1) w2;\]
\[\text{fst} (\alpha \ e1) \cap \text{mon-pl} (\text{map} \ α (e2 \ # \ w2)) = {} \implies P \ α (e1 \ # \ w1) w2\]
\[\implies P \ α (e1 \ # \ w1) (e2 \ # \ w2)\]
\[\implies P \ α w \ w\]
\[\langle \text{proof} \rangle\]

lemma [simp]: \[w \otimes_α [] = w\]
\[\langle \text{proof} \rangle\]

lemma cil-contains-empty[rule-format, simp]: [\[\in \ w a \otimes_α \ w b\] = \(wa=[] \land wb=[]\)]
\[\langle \text{proof} \rangle\]
**2.2 Properties of consistent interleaving**

— Consistent interleaving is a restriction of interleaving

**lemma** cil-subset-il: \( w \otimes \alpha w' \subseteq w \otimes w' \)

(proof)

**lemma** cil-subset-il': \( w \otimes w \otimes w \rightarrow w \otimes w \)

(proof)

**lemma** cil-set: \( w \otimes \alpha w \rightarrow set w = set w \cup set w \)

(proof)

**corollary** cil-mon-pl: \( w \otimes \alpha w \rightarrow mon-pl \ (map \ alpha w) = mon-pl \ (map \ alpha w1) \cup mon-pl \ (map \ alpha w2) \)

(proof)

**lemma** cil-length[rule-format]: \( \forall w \otimes \alpha w. \ length w = length wa + length wb \)

(proof)
lemmas

\textbf{lemma} \textit{cil-ileq}: \( w \in w_1 \otimes_\alpha w_2 \implies w_1 \leq w \land w_2 \leq w \)

\textit{proof}

\textbf{lemma} \textit{cil-commute}: \( w \otimes_\alpha w' = w' \otimes_\alpha w \)

\textit{proof}

\textbf{lemma} \textit{cil-assoc1}: \![ \begin{array}{c} w \in w_1 \otimes_\alpha w_3 \\
\land w \in w_1 \otimes_\alpha w_2 \end{array} \!]

\implies \exists \, w_r. \, w \in w_1 \otimes_\alpha w_r \land w_r \in w_2 \otimes_\alpha w_3

\textit{proof}

\textbf{lemma} \textit{cil-assoc2}:

\textbf{assumes} A: \( w \in w_1 \otimes_\alpha w_r \) \textbf{and} B: \( w_r \in w_2 \otimes_\alpha w_3 \)

\textbf{shows} \( \exists \, w_l. \, w \in w_l \otimes_\alpha w_3 \land w_l \in w_1 \otimes_\alpha w_2 \)

\textit{proof}

\textbf{lemma} \textit{cil-map}: \( w \in w_1 \otimes_{(\alpha \circ f)} w_2 \implies \text{map } f \in \text{map } f \in w_1 \otimes_\alpha \text{ map } f \in w_2 \)

\textit{proof}

\textbf{end}

\section{3 Acquisition Histories}

\textbf{theory} \textit{AcquisitionHistory}

\textbf{imports} \textit{ConsInterleave}

\textbf{begin}

The concept of \textit{acquisition histories} was introduced by Kahlon, Ivancic, and Gupta [5] as a bounded size abstraction of executions that acquire and release locks that contains enough information to decide consistent interleavabilility. In this work, we use this concept for reentrant monitors. As in Section 2, we encode monitor usage information in pairs of sets of monitors, and regard lists of such pairs as (abstract) executions. An item \((E, U)\) of such a list describes a sequence of steps of the concrete execution that first enters the monitors in \(E\) and then passes through the monitors in \(U\). The monitors in \(E\) are never left by the execution. Note that due to the syntactic binding of monitors to the program structure, any execution of a single thread can be abstracted to a sequence of \((E, U)\)-pairs. Restricting the possible schedules (see Section 8) will allow us to also abstract executions reaching a single program point to a sequence of such pairs.

We want to decide whether two executions are interleavable. The key observation of [5] is, that two executions \(e\) and \(e'\) are not interleavable if and only if there is a conflicting pair \((m, m')\) of monitors, such that \(e\) enters (and never leaves) \(m\) and then uses \(m'\) and \(e'\) enters (and never leaves) \(m'\) and then uses \(m\).

An acquisition history is a map from monitors to set of monitors. The
acquisition history of an execution maps a monitor $m$ that is allocated at the end of the execution to all monitors that are used after or in the same step that finally enters $m$. Monitors that are not allocated at the end of an execution are mapped to the empty set. Though originally used for a setting without reentrant monitors, acquisition histories also work for our setting with reentrant monitors.

This theory contains the definition of acquisition histories and acquisition history interleavability, an ordering on acquisition histories that reflects the blocking potential of acquisition histories, and a mapping function from paths to acquisition histories that is shown to be compatible with monitor consistent interleaving.

### 3.1 Definitions

Acquisition histories are modeled as functions from monitors to sets of monitors. Intuitively $m' \in h m$ models that an execution finally is in $m$, and monitor $m'$ has been used (i.e. passed or entered) after or at the same time $m$ has been finally entered. By convention, we have $m \in h m$ or $h m = \emptyset$.

**Definition** $ah == \{ (h::'m \Rightarrow 'm set) . \forall m. h m = \emptyset \lor m \in h m \}$

**Lemma** $ah$-cases[cases set]: $[h \in ah; h m = \emptyset] \implies P ; m \in h m \implies P] \implies P$

### 3.2 Interleavability

Two acquisition histories $h1$ and $h2$ are considered interleavable, iff there is no conflicting pair of monitors $m1$ and $m2$, where a pair of monitors $m1$ and $m2$ is called conflicting iff $m1$ is used in $h2$ after entering $m2$ and, vice versa, $m2$ is used in $h1$ after entering $m1$.

**Definition** $ah-il ::= ('m \Rightarrow 'm set) \Rightarrow ('m \Rightarrow 'm set) \Rightarrow bool (\textbf{infix} \; [\ast] \; 65)$

**Where**

$h1 \; [\ast] \; h2 == \neg(\exists m1 \; m2. \; m1 \in h2 \; m2 \land m2 \in h1 \; m1)$

From our convention, it follows (as expected) that the sets of entered monitors (lock-sets) of two interleavable acquisition histories are disjoint

**Lemma** $ah-il$-lockset-disjoint:

$[h1 \in ah; h2 \in ah; h1 \; [\ast] \; h2] \implies h1 \; m = \emptyset \lor h2 \; m = \emptyset$

**Proof**

Of course, acquisition history interleavability is commutative

**Lemma** $ah-il$-commute: $h1 \; [\ast] \; h2 \implies h2 \; [\ast] \; h1$

**Proof**
3.3 Used monitors

Let’s define the monitors of an acquisition history, as all monitors that occur in the acquisition history.

**Definition**

\[
\text{mon-ah} :: \text{('m ⇒ 'm set) ⇒ 'm set}
\]

\[
\text{mon-ah} \ h = \bigcup \{ h(m) \mid m. \text{True} \}
\]

3.4 Ordering

The element-wise subset-ordering on acquisition histories intuitively reflects the blocking potential: The bigger the acquisition history, the fewer acquire-

sition histories are interleavable with it.

Note that the Isabelle standard library automatically lifts the subset ordering to functions, so we need no explicit definition here.

— The ordering is compatible with interleavability, i.e. smaller acquisition histories are more likely to be interleavable.

**Lemma ah-leq-il**: \[
[ [ h1; h1' ≤ h2 ] ] = [ [ h1'; h2' ] ]
\]

**Lemma ah-leq-il-left**: \[
[ [ h1; h1' ≤ h1 ] ] = [ [ h1; h2' ] ]
\]

**Lemma ah-leq-il-right**: \[
[ [ h1; h2; h2' ≤ h2 ] ] = [ [ h1; h2' ] ]
\]

3.5 Acquisition histories of executions

Next we define a function that abstracts from executions (lists of enter/use pairs) to acquisition histories.

**Primrec** \[
\alpha \ah :: (\text{'m set × 'm set}) \text{list} ⇒ \text{'m ⇒ 'm set}
\]

\[
\begin{align*}
\alpha \ah \ [] & = \{} \\
\alpha \ah \ (e \# w) \ m & = (\text{if } m \in \text{fst } e \ \text{then } \text{fst } e \cup \text{snd } e \cup \text{mon-pl } w \ \text{else } \alpha \ah \ w \ m)
\end{align*}
\]

— \alpha \ah generates valid acquisition histories

**Lemma ah-ah**: \[
\alpha \ah \ w \in \ah
\]

**Lemma ah-hd**: \[
\begin{align*}
\text{if } m \in \text{fst } e \ \text{then } \text{fst } e \cup \text{snd } e \cup \text{mon-pl } w \ \text{else } \alpha \ah \ w \ m
\end{align*}
\]

**Lemma ah-tl**: \[
\begin{align*}
\alpha \ah \ w \ m
\end{align*}
\]

**Lemma ah-cases**: \[
\begin{align*}
\alpha \ah \ w \ m;
\end{align*}
\]

©
\begin{proof}

** lemma \texttt{αah-cons-cases}[\texttt{cases set, case-names hd tl}]:

\[
\begin{aligned}
&x \in \alpha \texttt{ah}(\texttt{e} # w') \quad \text{if } m \in \texttt{fst e};
&m \in \texttt{fst e} \cup \texttt{snd e} \cup \texttt{mon-pl w}' \quad \Rightarrow \quad P;
&m \notin \texttt{fst e};
&x \in \alpha \texttt{ah} w' \quad \Rightarrow \quad P
\end{aligned}
\]
\end{proof}

** lemma \texttt{mon-ah-subset}: \texttt{mon-ah (αah w) ⊆ mon-pl w}
\begin{proof}

** lemma \texttt{αah-ileq}: \texttt{w1 ≤ w2 ⇒ αah w1 ≤ αah w2}
\begin{proof}

We can now prove the relation of monitor consistent interleavability and interleavability of the acquisition histories.

** lemma \texttt{ah-interleavable1}:

\[
\begin{aligned}
w \in \texttt{w1} \otimes \alpha \texttt{w2} \quad \Rightarrow \quad \alpha \texttt{ah (map α w1)} [\ast] \alpha \texttt{ah (map α w2)}
\end{aligned}
\]

— The lemma is shown by induction on the structure of the monitor consistent interleaving operator
\begin{proof}

** lemma \texttt{ah-interleavable2}:

\[
\begin{aligned}
\texttt{assumes A: \alpha \texttt{ah (map α w1)} [\ast] \alpha \texttt{ah (map α w2)}}
\texttt{shows \texttt{w1} \otimes \alpha \texttt{w2} \neq \{\}}
\end{aligned}
\]

— This lemma is shown by induction on the sum of the word lengths
\begin{proof}

Finally, we can state the relationship between monitor consistent interleaving and interleaving of acquisition histories

** theorem \texttt{ah-interleavable}:

\[
\begin{aligned}
(\alpha \texttt{ah (map α w1)} [\ast] \alpha \texttt{ah (map α w2)}) \leftrightarrow (\texttt{w1} \otimes \alpha \texttt{w2} \neq \{\})
\end{aligned}
\]
\begin{proof}

\textbf{3.6 Acquisition history backward update}

We define a function to update an acquisition history backwards. This function is useful for constructing acquisition histories in backward constraint systems.

** definition \texttt{ah-update} :: \texttt{('m ⇒ 'm set) ⇒ ('m set * 'm set) ⇒ 'm set ⇒ ('m ⇒ 'm set)}

\[
\begin{aligned}
\texttt{where ah-update h F M m} &= \texttt{if m} \in \texttt{fst F} \texttt{then f} \texttt{st F} \cup \texttt{s} \texttt{nd F} \cup \texttt{M else h m}
\end{aligned}
\]

Intuitively, \texttt{ah-update h (E, U) M m} means to prepend a step \texttt{(E, U)} to the acquisition history \texttt{h} of a path that uses monitors \texttt{M}. Note that we need the extra parameter \texttt{M}, since an acquisition history does not contain information
about the monitors that are used on a path before the first monitor that will not be left has been entered.

**lemma ah-update-cons:** $\alpha \text{ah} (e\#w) = \text{ah-update} (\alpha \text{ah} w) e (\text{mon-pl} w)$

(proof)

The backward-update function is monotonic in the first and third argument as well as in the used monitors of the second argument. Note that it is, in general, not monotonic in the entered monitors of the second argument.

**lemma ah-update-mono:** $[ h \leq h' ; F = F' ; M \subseteq M' ] \Rightarrow \text{ah-update} h F M \leq \text{ah-update} h' F' M'$

(proof)

**lemma ah-update-mono2:** $[ h \leq h' ; U \subseteq U' ; M \subseteq M' ] \Rightarrow \text{ah-update} h (E, U) M \leq \text{ah-update} h' (E, U') M'$

(proof)

end

4 Labeled transition systems

theory LTS imports Main begin

Labeled transition systems (LTS) provide a model of a state transition system with named transitions.

4.1 Definitions

An LTS is modeled as a ternary relation between start configuration, transition label and end configuration

**type-synonym** $(c,a) LTS = (c \times a \times c) \text{ set}$

Transitive reflexive closure

**inductive-set**

$tcl :: (c,a) LTS \Rightarrow (c,a \text{ list}) LTS$

for $t$

**where**

empty[simp]: $(c,,c) \in tcl t$

| cons[simp]: [ $(c,a,c') \in t ; (c',w,c'') \in tcl t ] \Rightarrow (c,a\#w,c'') \in tcl t$

4.2 Basic properties of transitive reflexive closure

**lemma tcl-empty-cons:** $(c,,c') \in tcl t \Rightarrow (c=c')$

(proof)

**lemma tcl-empty-simp[imp]:** $(c,,c') \in tcl t = (c=c')$

(proof)
lemma trcl-single[simp]: \((c, [a], c') \in trcl t\) = \((c, a, c') \in t\)
(proof)

lemma trcl-uncons: \((c, a \# w, c')\in trcl t \Rightarrow \exists ch . (c, a, ch)\in t \land (ch, w, c') \in trcl t\)
(proof)

lemma trcl-uncons-cases: 
\[\begin{array}{l}
(c, a \# w, c') \in trcl S; \\
\hspace{1em} \exists ch . (c, a, ch)\in S; (ch, w, c') \in trcl S \Rightarrow P
\end{array}\]
(proof)

lemma trcl-unconsE: \( (c, a \# w, c')\in trcl S \Rightarrow \exists ch . (c, a, ch)\in S \land (ch, w, c') \in trcl S \Rightarrow P\)
(proof)

lemma trcl-uncons-cases: 
\[\begin{array}{l}
(c, a \# w, c') \in trcl S; \\
\hspace{1em} \exists ch . (c, a, ch)\in S; (ch, w, c') \in trcl S \Rightarrow P
\end{array}\]
(proof)

lemma trcl-one-elem: \((c, a, c') \in trcl t \Rightarrow (c, [e], c') \in trcl t\)
(proof)

lemma trcl-one-elem: \((c, a, c') \in trcl t \Rightarrow (c, [e], c') \in trcl t\)
(proof)

lemma trcl-one-elem: \((c, a, c') \in trcl t \Rightarrow (c, [e], c') \in trcl t\)
(proof)

lemma trcl-one-elem: \((c, a, c') \in trcl t \Rightarrow (c, [e], c') \in trcl t\)
(proof)

4.2.1 Appending of elements to paths

lemma trcl-rev-cons: \[\hspace{1em} (c, w, ch)\in trcl T; \ (ch, e, c')\in T \Rightarrow (c, w \# e, c')\in trcl T\]
(proof)

lemma trcl-rev-uncons: \((c, w \# e, c')\in trcl T \Rightarrow \exists ch . (c, w, ch)\in trcl T \land (ch, e, c')\in T\)
(proof)

lemma trcl-rev-induct[induct set, consumes 1, case-names empty snoc]: \!! c'. 
\[\hspace{1em} (c, w, c')\in trcl S; \]
\[\hspace{1em} \exists c. P \ c \ [\ c; \ !c w \ c' e c''. [ (c, w, c')\in trcl S; (c', e, c'')\in S; P c w c' ] \Rightarrow P c (w \# e) c'' \]
\[\Rightarrow P e w c'\]
(proof)

lemma trcl-rev-cases: \!! c c'. 
\[\hspace{1em} (c, w, c')\in trcl S; \]
\[\hspace{1em} [w=[]; c=c'\Rightarrow P; \]
\[\hspace{1em} !c w \ w h. [ w=w \# e]; (c, w, ch)\in trcl S; (ch, e, c')\in S \Rightarrow P\]
(proof)
lemma \textit{trcl-cons2}:
\[ (c,e,ch) \in T; (ch,f,e') \in T \implies (c,[e,f],e') \in \text{trcl } T \]

\begin{proof}
\end{proof}

4.2.2 Transitivity reasoning setup

\textbf{declare} \textit{trcl-cons2}[trans] — It’s important that this is declared before \textit{trcl-concat}, because we want \textit{trcl-concat} to be tried first by the transitivity reasoner

\textbf{declare} \textit{cons}[trans]

\textbf{declare} \textit{trcl-concat}[trans]

\textbf{declare} \textit{trcl-rev-cons}[trans]

4.2.3 Monotonicity

\textbf{lemma} \textit{trcl-mono}:
\[ \forall A B. A \subseteq B \implies \text{trcl } A \subseteq \text{trcl } B \]

\begin{proof}
\end{proof}

\textbf{lemma} \textit{trcl-inter-mono}:
\[ x \in \text{trcl } (S \cap R) \implies x \in \text{trcl } S x \in \text{trcl } (S \cap R) \implies x \in \text{trcl } R \]

\begin{proof}
\end{proof}

4.2.4 Special lemmas for reasoning about states that are pairs

\textbf{lemmas} \textit{trcl-pair-induct} = \textit{trcl-induct}[of \ (xc1,xc2) \ x (xa1,xa2), split-format (complete), consumes 1, case-names empty cons]

\textbf{lemmas} \textit{trcl-rev-pair-induct} = \textit{trcl-rev-induct}[of \ (xc1,xc2) \ x (xa1,xa2), split-format (complete), consumes 1, case-names empty snoc]

4.2.5 Invariants

\textbf{lemma} \textit{trcl-prop-trans}[cases set, consumes 1, case-names empty steps]:
\[ (c,w,c') \in \text{trcl } S;
\quad [c=c', w=[]] \implies P;
\quad [c \in \text{Domain } S; c' \in \text{Range } (\text{Range } S)] \implies P \]

\begin{proof}
\end{proof}

5 Thread Tracking

\textbf{theory} \textit{ThreadTracking}

\textbf{imports} \textit{Main} \sim\sim\sim /src/HOL/Library/Multiset LTS Misc

\begin{proof}
\end{proof}

This theory defines some general notion of an interleaving semantics. It defines how to extend a semantics specified on a single thread and a context
to a semantic on multisets of threads. The context is needed in order to keep track of synchronization.

5.1 Semantic on multiset configuration

The interleaving semantics is defined on a multiset of stacks. The thread to make the next step is nondeterministically chosen from all threads ready to make steps.

**definition**

\[
gtr \text{ gtrs} \equiv \{ (\{\#s\#\}+c,e,\{\#s'\#\}+c') | s \neq c \neq s' \neq c' \} \}
\]

**lemma** \(gtr1-s\): \((s,c),e,(s',c')\)\\(\varepsilon\)gtrs \(\Rightarrow\) \((\{\#s\#\}+c,e,\{\#s'\#\}+c')\)\\(\varepsilon\)\(gtr \text{ gtrs}\)

**lemma** \(gtr1\): \((s,c),w,(s',c')\)\\(\varepsilon\)\(trcl \text{ gtrs}\)

**lemma** \(gtrE\): 

\[(c,e,c',c')\in gtr T;\]

\(!s ce s' ce'. [c=\{\#s\#\}+ce; c'=\{\#s'\#\}+ce'; ((s,ce),e,(s',ce'))\in T] \Rightarrow P\]

**lemma** \(gtr-empty-conf-s[simp]\):

\((\{\#\},w,c')\notin gtr S\)

**lemma** \(gtr-empty-conf1[simp]\): \((\{\#\},w,c')\in trcl (gtr S) \leftrightarrow (w=\ [] \land c'=\[\#\])\)

**lemma** \(gtr-empty-conf2[simp]\): \((c,w,\{\#\})\in trcl (gtr S) \leftrightarrow (w=\ [] \land c=\[\#\])\)

**lemma** \(gtr-find-thread\): 

\[(c,e,c',c')\in gtr gtrs;\]

\(!s ce s' ce'. [c=\{\#s\#\}+ce; c'=\{\#s'\#\}+ce'; ((s,ce),e,(s',ce'))\in gtrs]\) \Rightarrow P\]

**lemma** \(gtr-step-cases[cases set, case-names loc other]\): 

\((\{\#s\#\}+ce,e,\{\#s'\#\}+c')\in \varepsilon gtr gtrs;\]

\(!s' ce'. [c=\{\#s'\#\}+ce'; ((s,ce),e,(s',ce'))\in gtrs]\) \Rightarrow P;\]

\(!c e c' ce'. [c=\{\#ss\#\}+ce; c'=\{\#ss'\#\}+ce'; ((ss,\{\#s\#\}+ce),e,(ss',ce'))\in gtrs]\) \Rightarrow P\]

\[(c,e,\{\#s'\#\}+ce')\in gtr gtrs;\]
5.2 Invariants

**Lemma gtr-preserve-s**:

\[(c,e,c') \in gtr T;\]
\[P c; \]
\[!!s c s' c' e. \lbrack P \{\#s\#\} + c; ((s,c),e,(s',c')) \in T \rbrack \implies P \{\#s'\#\} + c'\]
\[
\]
\[
\]
\[
\]
\[
\]
\[
\]

**Lemma gtr-preserve**:

\[(c,w,c') \in trcl (gtr T);\]
\[P c; \]
\[!!s c s' c' e. \lbrack P \{\#s\#\} + c; ((s,c),e,(s',c')) \in T \rbrack \implies P \{\#s'\#\} + c'\]
\[
\]
\[
\]
\[
\]
\[
\]

5.3 Context preservation assumption

We now assume that the original semantics does not modify threads in the context, i.e. it may only add new threads to the context and use the context to obtain monitor information, but not change any existing thread in the context. This assumption is valid for our semantics, where the context is just needed to determine the set of allocated monitors. It allows us to generally derive some further properties of such semantics.

**Locale env-no-step** =

**Fixes** gtrs :: \((s \times s \text{ multiset},'l)\) LTS

**Assumes** env-no-step-s[cases set, case-names csp]:
\[\lbrack ((s,c),e,(s',c')) \in gtrs; \nonumber \parallel \text{csp. } c' \leftarrow \text{csp} + c \implies P \rbrack \implies P\]

— The property of not changing existing threads in the context transfers to paths

**Lemma (in env-no-step) env-no-step[cases set, case-names csp]**:
\[\lbrack ((s,c),w,(s',c')) \in trcl gtrs; \nonumber \parallel \text{csp. } c' \leftarrow \text{csp} + c \implies P \rbrack \implies P\]

The following lemma can be used to make a case distinction how a step operated on a given thread in the end configuration:

**Loc** The thread made the step

**Spawn** The thread was spawned by the step
env The thread was not involved in the step

lemma (in env-no-step) rev-cases-p[cases set, case-names loc spawn env]:
assumes STEP: (c,e,\{#s'#\}+ce')\in gtrs gtr
LOC: !!s ce. [ c=\{#s#\}+ce; ((s,ce),e,(s',ce'))\in gtrs ] \implies P and
SPAWN: !!ss ss' ce csp.
\[ c=\{#ss#\}+ce; ce'=\{#ss'#\}+csp+ce; \\
   ((ss,ce),e,(ss',\{#ss'#\}+csp+ce))\in gtrs \]
\implies P and
ENV: !!ss ss' ce csp.
\[ c=\{#ss#\}+\{#s'#\}+ce; ce'=\{#ss'#\}+csp+ce; \\
   ((ss,\{#ss'#\}+ce),e,(ss',csp+\{#ss'#\}+ce))\in gtrs \]
\implies P
shows P
⟨proof⟩

5.4 Explicit local context

In the multiset semantics, a single thread has no identity. This may become a problem when reasoning about a fixed thread during an execution. For example, in our constraint-system-based approach the operational characterization of the least solution of the constraint system requires to state properties of the steps of the initial thread in some execution. With the multiset semantics, we are unable to identify those steps among all steps. There are many solutions to this problem, for example, using thread ids either as part of the thread’s configuration or as part of the whole configuration by using lists of stacks or maps from ids to stacks as configuration datatype.

In the following we present a special solution that is strong enough to suit our purposes but not meant as a general solution.

Instead of identifying every single thread uniquely, we only distinguish one thread as the local thread. The other threads are environment threads. We then attach to every step the information whether it was on the local or on some environment thread.

We call this semantics loc/env-semantics in contrast to the multiset-semantics of the last section.

5.4.1 Lifted step datatype
datatype 'a el-step = LOC 'a | ENV 'a
definition
loc w == filter (\lambda e. case e of LOC a \Rightarrow True \mid ENV a \Rightarrow False) w
definition
env w == filter (\lambda e. case e of LOC a \Rightarrow False \mid ENV a \Rightarrow True) w
definition
le-rem-s e == case e of LOC a ⇒ a | ENV a ⇒ a

Standard simplification lemmas

lemma loc-env-simps[simp]:
loc [] = []
env [] = []
⟨proof⟩

lemma loc-single[simp]: loc [a] = (case a of LOC e ⇒ [a] | ENV e ⇒ [])
⟨proof⟩

lemma loc-uncons[simp]:
loc (a#b) = (case a of LOC e ⇒ [a] | ENV e ⇒ [])@loc b
⟨proof⟩

lemma loc-unconc[simp]: loc (a@b) = loc a @ loc b
⟨proof⟩

lemma env-single[simp]: env [a] = (case a of LOC e ⇒ [] | ENV e ⇒ [a])
⟨proof⟩

lemma env-uncons[simp]:
env (a#b) = (case a of LOC e ⇒ [] | ENV e ⇒ [a]) @ env b
⟨proof⟩

lemma env-unconc[simp]: env (a@b) = env a @ env b
⟨proof⟩

The following simplification lemmas are for converting between paths of the
multiset- and loc/env-semantics

lemma le-rem-simps [simp]:
le-rem-s (LOC a) = a
le-rem-s (ENV a) = a
⟨proof⟩

lemma le-rem-id-simps[simp]:
le-rem-soLOC = id
le-rem-soENV = id
⟨proof⟩

lemma le-rem-id-map[simp]:
map le-rem-s (map LOC w) = w
map le-rem-s (map ENV w) = w
⟨proof⟩

lemma env-map-env [simp]: env (map ENV w) = map ENV w
⟨proof⟩

lemma env-map-loc [simp]: env (map LOC w) = []
⟨proof⟩

lemma loc-map-env [simp]: loc (map ENV w) = []
⟨proof⟩

lemma loc-map-loc [simp]: loc (map LOC w) = map LOC w

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\[\text{proof}\]

5.4.2 Definition of the loc/env-semantics

**type-synonym**

's el-conf = ('s × 's multiset)

**inductive-set**

gtrp :: ('s el-conf,'l) LTS ⇒ ('s el-conf,'l el-step) LTS

for S

where

gtrp-loc: ((s,c),(s',c'))∈S ⇒ ((s,c),LOC e,(s',c'))∈gtrp S

| gtrp-env: ((s,#{s#})+c),e,(s',#{s#}+c')∈S

⇒ ((sl,#{s#}+c),ENV e,(sl,#{s#}+c')∈gtrp S

5.4.3 Relation between multiset- and loc/env-semantics

**lemma**

gtrp2gtr-s:

\((s,c),e,(s',c')\)∈gtrp T \implies ((#{s#}+c,le-rem-s e,#{s'#}+c')\)∈gtr T

\(\text{proof}\)

**lemma**

gtrp2gtr:

\((s,c),w,(s',c')\)∈trcl (gtr T)

⇒ ((#{s#}+c,map le-rem-s w,#{s'#}+c')\)∈trcl (gtr T)

\(\text{proof}\)

**lemma** (in env-no-step) gtr2gtr-s[cases set, case-names gtrp]:

assumes A: \((#{s#}+c,w,c')\)∈gtr gtrs

and CASE: !s' ce' ee. \[c'=#{s'#}+ce'; ee=le-rem-s ee;\]

\((s,c),ee,(s',ce')\)∈gtrp gtrs

⇒ P

\(\text{proof}\)

**lemma** (in env-no-step) gtr2gtr[cases set, case-names gtrp]:

assumes A: \((#{s#}+c,w,c')\)∈trcl (gtr gtrs)

and CASE: !s' ce' ee. \[c'=#{s'#}+ce'; ee=map le-rem-s wv;\]

\((s,c),wv,(s',ce')\)∈trcl (gtrp gtrs)

⇒ P

\(\text{proof}\)

5.4.4 Invariants

**lemma**

gtrp-preserve-s:

assumes A: \((s,c),e,(s',c')\)∈gtrp T

and INIT: P \((\#{s#}+c)\)

and PRES: !s c s' c' e. [P \((\#{s#}+c)\); \((s,c),e,(s',c')\)∈T]

⇒ P \((\#{s'#}+c')\)

\(\text{proof}\)
lemma gtrp-preserve:
  assumes A: \((s,c), w, (s',c')\) ∈ trcl (gtrp T)
  and INIT: \(P (\{\#s\#\}+c)\)
  and PRES: \(!s \land s' \land e. [P (\{\#s\#\}+c); ((s,c), e, (s',c')) \in T]\)
  shows \(P (\{\#s'\#\}+c')\)
  (proof)

end

6 Flowgraphs

theory Flowgraph
imports Main Misc
begin

We use a flowgraph-based program model that extends the one we used previously [6]. A program is represented as an edge annotated graph and a set of procedures. The nodes of the graph are partitioned by the procedures, i.e. every node belongs to exactly one procedure. There are no edges between nodes of different procedures. Every procedure has a distinguished entry and return node and a set of monitors it synchronizes on. Additionally, the program has a distinguished main procedure. The edges are annotated with statements. A statement is either a base statement, a procedure call or a thread creation (spawn). Procedure calls and thread creations refer to the called procedure or to the initial procedure of the spawned thread, respectively.

We require that the main procedure and any initial procedure of a spawned thread does not synchronize on any monitors. This avoids that spawning of a procedure together with entering a monitor is available in our model as an atomic step, which would be an unrealistic assumption for practical problems. Technically, our model would become strictly more powerful without this assumption.

If we allowed this, our model would become strictly more powerful,

6.1 Definitions

datatype ('p,'ba) edgeAnnot = Base 'ba | Call 'p | Spawn 'p

type-synonym ('n,'p,'ba) edge = ('n × ('p,'ba) edgeAnnot × 'n)

record ('n,'p,'ba,'m) flowgraph-rec =
  edges :: ('n,'p,'ba) edge set — Set of annotated edges
  main :: 'p — Main procedure
  entry :: 'p ⇒ 'n — Maps a procedure to its entry point

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return :: 'p ⇒ 'n — Maps a procedure to its return point
mon :: 'p ⇒ 'm set — Maps procedures to the set of monitors they allocate
proc-of :: 'n ⇒ 'p — Maps a node to the procedure it is contained in

**definition**

initialproc fg p == p=main fg ∨ (∃ u v. (u, Spawn p, v) ∈ edges fg)

**lemma** main-is-initial[simp]: initialproc fg (main fg)
⟨proof⟩

**locale** flowgraph =
fixes fg :: ('n,'p,'ba,'m,'more) flowgraph-rec-scheme (structure)

— Edges are inside procedures only
assumes edges-part: (u,a,v) ∈ edges fg ⇒ proc-of fg u = proc-of fg v
— The entry point of a procedure must be in that procedure
assumes entry-valid[simp]: proc-of fg (entry fg p) = p
— The return point of a procedure must be in that procedure
assumes return-valid[simp]: proc-of fg (return fg p) = p
— Initial procedures do not synchronize on any monitors
assumes initial-no-mon[simp]: initialproc fg p ⇒ mon fg p = {}

6.2 Basic properties

**lemma** (in flowgraph) spawn-no-mon[simp]:
(u, Spawn p, v) ∈ edges fg ⇒ mon fg p = {}
⟨proof⟩

**lemma** (in flowgraph) main-no-mon[simp]: mon fg (main fg) = {}
⟨proof⟩

**lemma** (in flowgraph) entry-return-same-proc[simp]:
entry fg p = return fg p' ⇒ p=p'
⟨proof⟩

**lemma** (in flowgraph) entry-entry-same-proc[simp]:
entry fg p = entry fg p' ⇒ p=p'
⟨proof⟩

**lemma** (in flowgraph) return-return-same-proc[simp]:
return fg p = return fg p' ⇒ p=p'
⟨proof⟩

6.3 Extra assumptions for flowgraphs

In order to simplify the definition of our restricted schedules (cf. Section 8),
we make some extra constraints on flowgraphs. Note that these are no real
restrictions, as we can always rewrite flowgraphs to match these constraints,
preserving the set of conflicts. We leave it to future work to consider such a
rewriting formally.
The background of this restrictions is that we want to start an execution of a thread with a procedure call that never returns. This will allow easier technical treatment in Section 8. Here we enforce this semantic restrictions by syntactic properties of the flowgraph.

The return node of a procedure is called isolated, if it has no incoming edges and is different from the entry node. A procedure with an isolated return node will never return. See Section 8.1 for a proof of this.

\[ \text{isolated-ret \ fg \ p} = (\forall u l. \neg(u,l,\text{return \ fg \ p}) \in \text{edges \ fg}) \land \text{entry \ fg \ p} \neq \text{return \ fg \ p} \]

The following syntactic restrictions guarantee that each thread’s execution starts with a non-returning call. See Section 8.1 for a proof of this.

**locale** eflowgraph = flowgraph +

— Initial procedure’s entry node isn’t equal to its return node

**assumes** initial-no-ret: initialproc \ fg \ p \implies \text{entry \ fg \ p} \neq \text{return \ fg \ p}

— The only outgoing edges of initial procedures’ entry nodes are call edges to procedures with isolated return node

**assumes** initial-call-no-ret: [ initialproc \ fg \ p; (entry \ fg \ p,l,v) \in \text{edges \ fg} ] \implies \exists p’. l=Call \ p’ \land \text{isolated-ret \ fg \ p’}

### 6.4 Example Flowgraph

This section contains a check that there exists a (non-trivial) flowgraph, i.e. that the assumptions made in the flowgraph and eflowgraph locales are consistent and have at least one non-trivial model.

**definition** example-fg ==

\[ \text{edges} = \{(0::nat,0::nat),\text{Call} \ 1,(0,1)), ((1,0),\text{Spawn} \ 0,(1,0)), ((1,0),\text{Call} \ 0, (1,0))\}, \]

main = 0,
entry = λp. (p,0),
return = λp. (p,1),
m = λp. if p=1 then \{0\} else {},
proc-of = λ (p,x). p \}

**lemma** exists-eflowgraph: eflowgraph example-fg

(proof)

**end**

### 7 Operational Semantics

**theory** Semantics

**imports** Main Flowgraph ~~/src/HOL/Library/Multiset LTS Interleave ThreadTracking
begin

7.1 Configurations and labels

The state of a single thread is described by a stack of control nodes. The
top node is the current control node and the nodes deeper in the stack are
stored return addresses. The configuration of a whole program is described
by a multiset of stacks.

Note that we model stacks as lists here, the first element being the top
element.

\textbf{type-synonym} \texttt{'n conf} = (\texttt{'n list}) multiset

A step is labeled according to the executed edge. Additionally, we introduce
a label for a procedure return step, that has no corresponding edge.

\textbf{datatype} (\texttt{'p, 'ba}) label = LBase \texttt{'ba} | LCall \texttt{'p} | LRet | LSpawn \texttt{'p}

7.2 Monitors

The following defines the monitors of nodes, stacks, configurations, step
labels and paths (sequences of step labels)

\textbf{definition} — The monitors of a node are the monitors the procedure of the node synchronizes
on
\texttt{mon-n fg n} \texttt{==} \texttt{mon fg} (\texttt{proc-of fg n})

\textbf{definition} — The monitors of a stack are the monitors of all its nodes
\texttt{mon-s fg s} \texttt{==} \bigcup \{ \texttt{mon-n fg n} \mid \texttt{n . n} \in \texttt{set s} \}

\textbf{definition} — The monitors of a configuration are the monitors of all its stacks
\texttt{mon-c fg c} \texttt{==} \bigcup \{ \texttt{mon-s fg s} \mid \texttt{s . s :\# c} \}

— The monitors of a step label are the monitors of procedures that are called by
this step

\textbf{definition} \texttt{mon-e :: ('}b, 'c, 'd, 'a, 'e\texttt{) flowgraph-rec-scheme} \Rightarrow ('c, 'f) label \Rightarrow 'a
\texttt{set where}
\texttt{mon-e fg e = (case e of (LCall p) \Rightarrow mon fg p | \cdot \Rightarrow \{\})}

\textbf{lemma} \texttt{mon-e-simps [simp]}:
\texttt{mon-e fg (LBase a) = \{\}}
\texttt{mon-e fg (LCall p) = mon fg p}
\texttt{mon-e fg (LRet) = \{\}}
\texttt{mon-e fg (LSpawn p) = \{\}}
\texttt{(proof)}

\textbf{definition}
\texttt{mon-w fg w} \texttt{==} \bigcup \{ \texttt{mon-e fg e} \mid \texttt{e . e} \in \texttt{set w}\}
\textbf{lemma} mon-s-alt: \(\text{mon-s}\ fg\ s == \bigcup (\text{mon}\ fg\ '\ \text{proc-of}\ fg\ '\ \text{set}\ s)\)
\langle proof \rangle

\textbf{lemma} mon-c-alt: \(\text{mon-c}\ fg\ c == \bigcup (\text{mon}\ fg\ '\ \text{set-mset}\ c)\)
\langle proof \rangle

\textbf{lemma} mon-w-alt: \(\text{mon-w}\ fg\ w == \bigcup (\text{mon}\ fg\ '\ \text{set}\ w)\)
\langle proof \rangle

\textbf{lemma} mon-sI: \[\n\in\ \text{set}\ s; \ m\in\text{mon-n}\ fg\ n\] \implies m\in\text{mon-s}\ fg\ s
\langle proof \rangle

\textbf{lemma} mon-sD: m\in\text{mon-s}\ fg\ s \implies \exists n\in\text{set}\ s. m\in\text{mon-n}\ fg\ n
\langle proof \rangle

\textbf{lemma} mon-n-same-proc: \(\text{proc-of}\ fg\ n = \text{proc-of}\ fg\ n' \implies \text{mon-n}\ fg\ n = \text{mon-n}\ fg\ n'\)
\langle proof \rangle

\textbf{lemma} mon-s-same-proc: \(\text{proc-of}\ fg\ '\ \text{set}\ s = \text{proc-of}\ fg\ '\ \text{set}\ s' \implies \text{mon-s}\ fg\ s = \text{mon-s}\ fg\ s'\)
\langle proof \rangle

\textbf{lemma} (in flowgraph) mon-of-entry[simp]: \(\text{mon-n}\ fg\ (\text{entry}\ fg\ p) = \text{mon}\ fg\ p\)
\langle proof \rangle

\textbf{lemma} (in flowgraph) mon-of-ret[simp]: \(\text{mon-n}\ fg\ (\text{return}\ fg\ p) = \text{mon}\ fg\ p\)
\langle proof \rangle

\textbf{lemma} mon-c-single[simp]: \(\text{mon-c}\ fg\ \{\#\}\ = \text{mon-s}\ fg\ s\)
\langle proof \rangle

\textbf{lemma} mon-s-single[simp]: \(\text{mon-s}\ fg\ [n] = \text{mon-n}\ fg\ n\)
\langle proof \rangle

\textbf{lemma} mon-s-empty[simp]: \(\text{mon-s}\ fg\ [] = \{\}\)
\langle proof \rangle

\textbf{lemma} mon-c-empty[simp]: \(\text{mon-c}\ fg\ [#] = \{\}\)
\langle proof \rangle

\textbf{lemma} mon-s-unconc: \(\text{mon-s}\ fg\ (a\@b) = \text{mon-s}\ fg\ a \cup \text{mon-s}\ fg\ b\)
\langle proof \rangle

\textbf{lemma} mon-s-uncons[simp]: \(\text{mon-s}\ fg\ (a\#as) = \text{mon-n}\ fg\ a \cup \text{mon-s}\ fg\ as\)
\langle proof \rangle

\textbf{lemma} mon-c-unconc: \(\text{mon-c}\ fg\ (a+b) = \text{mon-c}\ fg\ a \cup \text{mon-c}\ fg\ b\)
\langle proof \rangle

\textbf{lemma} mon-cl: \[s:\#c; m\in\text{mon-s}\ fg\ s\] \implies m\in\text{mon-c}\ fg\ c
\langle proof \rangle

\textbf{lemma} mon-cD: \[m\in\text{mon-c}\ fg\ c\] \implies \exists s. s:\#c \land m\in\text{mon-s}\ fg\ s
\langle proof \rangle

\textbf{lemma} mon-s-mono: set s \subseteq set s' \implies \text{mon-s}\ fg\ s \subseteq \text{mon-s}\ fg\ s'
\langle proof \rangle
lemma mon-c-mono: \(c \leq c' \implies \text{mon-c fg} c \subseteq \text{mon-c fg} c'\)

lemma mon-w-empty[simp]: \(\text{mon-w fg} [] = \{\}\)

lemma mon-w-single[simp]: \(\text{mon-w fg} [e] = \text{mon-e fg} e\)

lemma mon-w-unconc: \(\text{mon-w fg} (wa@wb) = \text{mon-w fg} wa \cup \text{mon-w fg} wb\)

lemma mon-w-uncons[simp]: \(\text{mon-w fg} (e # w) = \text{mon-e fg} e \cup \text{mon-w fg} w\)

lemma mon-w-ileq: \(w \preceq w' \implies \text{mon-w fg} w \subseteq \text{mon-w fg} w'\)

7.3 Valid configurations

We call a configuration valid if each monitor is owned by at most one thread.

definition valid fg c == \(\forall s s'. (\#s\#)+\{\#s'\#\} \leq c \longrightarrow \text{mon-s fg} s \cap \text{mon-s fg} s' = \{\}\)

lemma valid-empty[simp, intro!]: \(\text{valid fg} \{\}\)

lemma valid-single[simp, intro!]: \(\text{valid fg} \{#s\#\}

lemma valid-split1:
valid fg (c+c') \implies \text{valid fg} c \land \text{valid fg} c' \land \text{mon-c fg} c \cap \text{mon-c fg} c' = \{\}

lemma valid-split2:
\([\text{valid fg} c; \text{valid fg} c'; \text{mon-c fg} c \cap \text{mon-c fg} c' = \{\}] \implies \text{valid fg} (c+c')\)

lemma valid-unconc:
\(\text{valid fg} (c+c') \longleftrightarrow (\text{valid fg} c \land \text{valid fg} c' \land \text{mon-c fg} c \cap \text{mon-c fg} c' = \{\})\)

lemma valid-no-mon: \(\text{mon-c fg} c = \{\} \implies \text{valid fg} c\)

7.4 Configurations at control points

— A stack is at \(U\) if its top node is from the set \(U\)

 primrec atU-s ::\ ('n set \Rightarrow 'n list \Rightarrow bool)
where
\(\text{atU-s U} [] = \text{False}\)
| \(\text{atU-s U} (w#@r) = (w\in U)\)

lemma atU-s-decomp[simp]: \(\text{atU-s U} (s@#s') = (\text{atU-s U} s \lor (s=[] \land \text{atU-s U} s'))\)
(proof)
definition  
\sU\ U\ c \equiv \exists\ s.\ s:\#\ c \land \sU-s\ U\ s

lemma \sU-fmt: \begin{align*} [\ \begin{array}{l} \sU\ U\ c; \forall ai\ r.\ [ui\#r :\#\ c;\ ui\in U] \implies P \end{array} ] \implies P \end{align*}
(\proof)

lemma \sU-union-cases\{\text{case-names left right, consumes 1}\}: \begin{align*} & \begin{array}{l} \sU\ U\ (c1+c2); \\ \sU\ U\ c1 \implies P; \\ \sU\ U\ c2 \implies P \end{array} \\ \implies P \end{align*}
(\proof)

lemma \sU-add: \sU\ U\ c \implies \sU\ U\ (c+c) \land \sU\ U\ (c+c)
(\proof)

lemma \sU-union\{simp\}: \sU\ U\ (c1+c2) = (\sU\ U\ c1 \lor \sU\ U\ c2)
(\proof)

lemma \sU-empty\{simp\}: \neg \sU\ U\ \{\#\}
(\proof)

lemma \sU-single\{simp\}: \sU\ U\ \{\#s\#\} = \sU-s\ U\ s
(\proof)

lemma \sU-single-top\{simp\}: \sU\ U\ \{\#u\#r\#\} = (u\in U)
(\proof)

lemma \sU-xchange-stack: \sU\ U\ (\{\#u\#r\#\}+c) \implies \sU\ U\ (\{\#u\#r'\#\}+c)
(\proof)
definition  
\sUV\ U\ V\ c \equiv \exists\ su\ sv.\ \{\#su\#\}+\{\#sv\#\} \leq\#\ c \land \sU-s\ U\ su \land \sU-s\ V\ sv

lemma \sUV-empty\{simp\}: \neg \sUV\ U\ V\ \{\#\}
(\proof)

lemma \sUV-single\{simp\}: \neg \sUV\ U\ V\ \{\#s\#\}
(\proof)

lemma \sUV-union\{simp\}: 
\sUV\ U\ V\ (c1+c2) \iff (\begin{align*} & \begin{array}{l} (\sUV\ U\ V\ c1) \lor \\ (\sUV\ U\ V\ c2) \lor \\ (\sU\ U\ c1 \land \sU\ V\ c2) \lor \\ (\sU\ V\ c1 \land \sU\ U\ c2) \end{array} \end{align*})
(\proof)

lemma \sUV-union-cases\{\text{case-names left right lr rl, consumes 1}\}: \begin{align*} & \begin{array}{l} \sUV\ U\ V\ (c1+c2); \end{array} \end{align*}
7.5 Operational semantics

7.5.1 Semantic reference point

We now define our semantic reference point. We assess correctness and completeness of analyses relative to this reference point.

\[
\text{atUV } U \ V \ c1 \Rightarrow P; \\
\text{atUV } U \ V \ c2 \Rightarrow P; \\
[\text{atU } U \ c1; \text{atU } V \ c2] \Rightarrow P; \\
[\text{atU } V \ c1; \text{atU } U \ c2] \Rightarrow P \\
\Rightarrow P
\]

\[\text{⟨proof⟩}\]

Instead of working directly with the reference point semantics, we define the operational semantics of flowgraphs by describing how a single stack is transformed in a context of environment threads, and then use the theory developed in Section 5 to derive an interleaving semantics. Note that this semantics is also defined for invalid configurations (cf. Section 7.3). In Section 7.6.1 we will show that it preserves validity of a configuration, and in Section 7.6.2 we show that it is equivalent to the reference point semantics.
on valid configurations.

**inductive-set**

\[
\text{trss} :: (\langle n, p, ba, n, more \rangle) \text{ flowgraph-rec-scheme } \Rightarrow \\
((\langle n \text{ list } * \langle n \text{ conf} \rangle \rangle) *) (\langle p, ba \rangle) \label{\langle n \text{ list } * \langle n \text{ conf} \rangle \rangle) \text{ set }
\]

**for** \(fg\)

**where**

\[
\text{trss-base: } \begin{cases} \langle u, \text{Base } a, v \rangle \in \text{edges } fg \Rightarrow \\
\langle u \# r, c \rangle, L\text{Base } a, \langle v \# r, c \rangle \in \text{trss } fg \\
\end{cases}
\]

| \text{trss-call: } \begin{cases} \langle u, \text{Call } p, v \rangle \in \text{edges } fg; \text{ mon } fg p \cap \text{ mon-c } fg c = \{ \} \Rightarrow \\
\langle u \# r, c \rangle, L\text{Call } p, \langle \langle \text{entry } fg p \# v \# r, c \rangle \rangle \in \text{trss } fg \\
\end{cases}
\]

| \text{trss-ret: } \begin{cases} \langle u, \text{return } fg p \# r, c \rangle, L\text{Ret}(r, c) \in \text{trss } fg \\
\end{cases}
\]

| \text{trss-spawn: } \begin{cases} \langle u, \text{Spawn } p, v \rangle \in \text{edges } fg \Rightarrow \\
\langle u \# r, c \rangle, L\text{Spawn } p, \langle v \# r, \langle \text{entry } fg p \# r, c \rangle \rangle \in \text{trss } fg \\
\end{cases}
\]

— The interleaving semantics is generated using the general techniques from Section 5

**abbreviation** \(tr\) **where** \(tr \ fg \equiv \ gtr \ (\text{trss } fg)\)

— We also generate the loc/env-semantics

**abbreviation** \(trp\) **where** \(trp \ fg \equiv \ gtrp \ (\text{trss } fg)\)

### 7.6 Basic properties

#### 7.6.1 Validity

**lemma (in flowgraph)** \(\text{trss-valid-preserve-s:} \)

\[
\langle \text{valid } fg \ (\{n \#\} + c); \langle s, c \rangle, \langle s', c' \rangle \rangle \in \text{trss } fg \Rightarrow \text{valid } fg \ (\{n \#\} + c')
\]

**proof**

**lemma (in flowgraph)** \(\text{trss-valid-preserve:} \)

\[
\langle \{s, c \}, w, \langle s', c' \rangle \rangle \in \text{trcl } (\text{trss } fg); \text{ valid } fg \ (\{n \#\} + c) \Rightarrow \text{valid } fg \ (\{n \#\} + c')
\]

**proof**

**lemma (in flowgraph)** \(\text{tr-valid-preserve-s:} \)

\[
\langle c, w, c' \rangle \in \text{trcl } (tr \ fg); \text{ valid } fg \ c \Rightarrow \text{valid } fg \ c'
\]

**proof**

**lemma (in flowgraph)** \(\text{tr-validator-preserve:} \)

\[
\langle c, w, c' \rangle \in \text{trcl } (tr \ fg); \text{ valid } fg \ c \Rightarrow \text{valid } fg \ c'
\]

**proof**

**lemma (in flowgraph)** \(\text{trp-valid-preserve-s:} \)

\[
\langle \{s, c \}, w, \langle s', c' \rangle \rangle \in \text{trcl } (trp \ fg); \text{ valid } fg \ (\{n \#\} + c) \Rightarrow \text{valid } fg \ (\{n \#\} + c')
\]

**proof**

**lemma (in flowgraph)** \(\text{trp-valid-preserve:} \)

\[
\langle \{s, c \}, w, \langle s', c' \rangle \rangle \in \text{trcl } (trp \ fg); \text{ valid } fg \ (\{n \#\} + c) \Rightarrow \text{valid } fg \ (\{n \#\} + c')
\]

**proof**

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7.6.2 Equivalence to reference point

— The equivalence between the semantics that we derived using the techniques from Section 5 and the semantic reference point is shown nearly automatically.

lemma refpoint-eq-s: valid fg c \implies (\langle c, e, c' \rangle \in \text{refpoint} \ fg) \iff (\langle c, e, c' \rangle \in \text{tr} \ fg)

⟨proof⟩

lemma (in flowgraph) refpoint-eq:
valid fg c \implies (\langle c, w, c' \rangle \in \text{trcl} (\text{refpoint} \ fg)) \iff (\langle c, w, c' \rangle \in \text{trcl} (\text{tr} \ fg))

⟨proof⟩

7.6.3 Case distinctions

lemma trss-c-cases-s[\text{cases set, case-names no-spawn spawn}]:
\[ ((s,c),e,(s',c')) \in \text{trss} \ fg; \]
\[ \text{!! } p \ u \ v. \ e=L\text{Spawn } p; (u,\text{Spawn } p,v) \in \text{edges} \ fg; \]
\[ \text{hd } s=u; \text{ hd } s'=v; \ c'=\#\text{ entry } fg p \#+c \]\n\[ \implies P \]

⟨proof⟩

lemma trss-c-fmt-s:
\[ ((s,c),e,(s',c')) \in \text{trss} \ fg; \]
\[ \text{!! } csp. \ c'=csp+c; \text{ mon-c } fg csp=\{} \]\n\[ \implies P \]

⟨proof⟩

lemma (in flowgraph) trss-c-split-s:
\[ ((s,c),e,(s',c')) \in \text{trss} \ fg; \]
\[ \text{!! } csp. \ c'=csp+c; \text{ mon-c } fg csp=\{} \]\n\[ \implies P \]

⟨proof⟩

lemma trss-c-cases[\text{cases set, case-names c-case}]: !! s. c.
\[ ((s,c),w,(s',c')) \in \text{trcl} (\text{trss} \ fg); \]
\[ \text{!! } csp. \ c'=csp+c; !! s. \ s:\#csp \implies \exists p \ u \ v. \ s=\text{entry } fg p \\& \]
\[ (u,\text{Spawn } p,v) \in \text{edges} \ fg \\& \]
\[ \text{initialproc } fg p \]\n\[ \implies P \]

⟨proof⟩

lemma (in flowgraph) c-of-initial-no-mon:
\text{assumes } A: !! s. s:\#csp \implies \exists p. \ s=\text{entry } fg p \\& \text{ initialproc } fg p\]
\text{shows } mon-c fg csp=\{}

⟨proof⟩

lemma (in flowgraph) trss-c-no-mon-s:
\text{assumes } A: ((s,c),e,(s',c')) \in \text{trss} \ fg\]
\text{shows } mon-c fg c'=\text{ mon-c } fg c
corollary (in flowgraph) trss-c-no-mon:

\[(s,c),w,(s',c')\in\text{trcl}(\text{trss } fg) \implies \text{mon-c } fg \ c' = \text{mon-c } fg \ c\]

(\text{proof})

lemma (in flowgraph) trss-spawn-no-mon-step[simp]:

\[(s,c),L\text{Spawn } p, (s',c')\in\text{trss } fg \implies \text{mon } fg \ p = \{\}\]

(\text{proof})

lemma trss-no-empty-s[simp]: \[(([],c),(s',c'))\in\text{trss } fg = \text{False}

(\text{proof})

lemma trss-no-empty[simp]:

assumes A: \[(([],c),(s',c'))\in\text{trcl}(\text{trss } fg)

shows \(w=[] \land s'=[] \land c = c'\)

(\text{proof})

lemma trs-step-cases[cases set, case-names NO-SPAWN SPAWN]:

assumes A: \((c,e,c')\in\text{tr } fg\)

assumes A-NO-SPAWN: !!(s ce s' csp. [\([s,ce,(s',ce)]\in\text{trss } fg;\]
\(c=\#s# + ce; \ c'=\#s'# + ce\]

[\] \(\implies P\)

assumes A-SPAWN: !!(s ce s' p. [\([s,ce),L\text{Spawn } p, (s',\#\text{entry } fg \ p\#+ce)]\in\text{trss } fg;\]
\(c=\#s# + ce;\)
\(c'=\#s'# + \#\text{entry } fg \ p\#+ce;\)
\(e=L\text{Spawn } p\]

[\] \(\implies P\)

shows P

(\text{proof})

7.7 Advanced properties

7.7.1 Stack composition / decomposition

lemma trss-stack-comp-s:

\[((s,c),e,(s',c'))\in\text{trss } fg \implies ((s\oplus r,c),e,(s'\oplus r,c'))\in\text{trss } fg\]

(\text{proof})

lemma trss-stack-comp:

\[((s,c),w,(s',c'))\in\text{trcl}(\text{trss } fg) \implies ((s\oplus r,c),w,(s'\oplus r,c'))\in\text{trcl}(\text{trss } fg)\]

(\text{proof})

lemma trss-stack-decomp-s: \[ ((s\oplus r,c),e,(s',c'))\in\text{trss } fg; s\ne[] \]

\(\implies \exists \ sp'. s'=sp'\oplus r \land ((s,c),e,(sp',c'))\in\text{trss } fg\]

(\text{proof})
lemma trss-find-return: \[
\text{(} (s \# r, c), w, (r', c') \text{)} \in \text{trcl (trss f)}; \\
\text{If } s = [], \text{ the proposition follows trivially}
\]
\langle \text{proof} \rangle

lemma trss-return-cases [cases set]: \text{!!u r c.} \]
\text{(} (s \# r, c), w, (r', c') \text{)} \in \text{trcl (trss f)}; \\
\text{If } s = [], \text{ the proposition follows trivially}
\langle \text{proof} \rangle

lemma trss-find-call: \text{!!v r' c'.} \]
\text{(} (\text{[}[sp], c), w, (v \# r', c') \text{)} \in \text{trcl (trss f)}; \\
\text{If } s = [], \text{ the proposition follows trivially}
\langle \text{proof} \rangle

lemma (in flowgraph) trss-find-call': \text{\textbf{assumes A:} (\text{[}[sp], c), w, (\text{return f}_p \# u', c') \text{)} \in trcl (trss f)} \\
\text{\textbf{and EX:} !!uh ch wa wb.} \]
\text{(} (\text{[}[uh], c), w, (\text{[}[uh], c') \text{)} \in \text{trcl (trss f)}; \\
\text{(} (\text{[}[uh], ch), \text{Call p, (entry f}_p \# u', ch) \text{)} \in \text{trss f}; \\
\text{u, Call p, u'} \in \text{edges f}; \\
\text{\textbf{shows} P}
\langle \text{proof} \rangle

lemma (in flowgraph) trss-bot-proc-const: \text{!!s u' c'.} (\text{(s}@[u], c), w, (\text{s}@[u'], c') \text{)} \in \text{trcl (trss f)} \\
\langle \text{proof} \rangle

lemma (in flowgraph) trss-er-path-proc-const: \text{\textbf{assumes A:} (\text{[}[entry f}_p \# q], c), w, (\text{return f}_p \# q, c') \text{)} \in \text{trcl (trss f)}; \\
\text{\textbf{shows} P \Rightarrow p = q}
\langle \text{proof} \rangle

lemma trss-2empty-to-2return: \text{[] (} (s, c), w, (\text{[}], c') \text{)} \in \text{trcl (trss f)}; \\
\exists w' p. w = w'@[LRet] \land (\text{(s, c), w', (return f}_p \# q, c') \text{)} \in \text{trcl (trss f)}
\section*{7.7.2 Adding threads}

\paragraph*{lemma trss-env-increasing-s: \((s,c,e,(s',c'))\) \(\in\text{trss} f g \Rightarrow c \leq \#e'\)}
\begin{proof}
\end{proof}

\paragraph*{lemma trss-env-increasing: \((s,c,w,(s',c'))\) \(\in\text{trcl} (\text{trss} f g) \Rightarrow c \leq \#e'\)}
\begin{proof}
\end{proof}

\section*{7.7.3 Conversion between environment and monitor restrictions}

\paragraph*{lemma trss-mon-e-no-ctx: \((s,c,e,(s',c'))\) \(\in\text{trss} f g \Rightarrow \text{mon-e} f g e \cap \text{mon-e} f g c = \{\}\)}
\begin{proof}
\end{proof}

\paragraph*{lemma (in flowgraph) trss-mon-w-no-ctx: \((s,c,w,(s',c'))\) \(\in\text{trcl} (\text{trss} f g) \Rightarrow \text{mon-w} f g w \cap \text{mon-e} f g c = \{\}\)}
\begin{proof}
\end{proof}

\paragraph*{lemma (in flowgraph) trss-modify-context-s: \([(s,c),e,(s',c')]\) \(\in\text{trss} f g; \text{mon-e} f g e \cap \text{mon-e} f g cn = \{\}\) \(\Rightarrow \exists\text{ csp. } c' = \text{csp} + c \land \text{mon-e} f g \text{ csp} = \{\}\) \land \((s,cn),e,(s',\text{csp}+cn))\) \(\in\text{trss} f g\)}
\begin{proof}
\end{proof}

\paragraph*{lemma (in flowgraph) trss-modify-context-rule-format: \([(s,c),w,(s',c')]\) \(\in\text{trcl} (\text{trss} f g)\) \(\Rightarrow \forall\text{ cn. } (s,c),w,(s',c')\) \(\in\text{trcl} (\text{trss} f g)\) \(\Rightarrow \exists\text{ csp. } c' = \text{csp} + c \land \text{mon-e} f g \text{ csp} = \{\}\) \land \((s,cn),w,(s',\text{csp}+cn))\) \(\in\text{trcl} (\text{trss} f g)\)}
\begin{proof}
\end{proof}

\paragraph*{lemma trss-add-context-s: \([(s,c),e,(s',c')]\) \(\in\text{trss} f g; \text{mon-e} f g e \cap \text{mon-e} f g ce = \{\}\) \(\Rightarrow ((s,c+ce),e,(s',c'+ce))\) \(\in\text{trss} f g\)}
\begin{proof}
\end{proof}

\paragraph*{lemma trss-add-context: \([(s,c),w,(s',c')]\) \(\in\text{trcl} (\text{trss} f g); \text{mon-w} f g w \cap \text{mon-e} f g ce = \{\}\) \(\Rightarrow ((s,c+ce),w,(s',c'+ce))\) \(\in\text{trcl} (\text{trss} f g)\)}
\begin{proof}
\end{proof}

\paragraph*{lemma trss-drop-context-s: \([(s,c),e,(s',c'+ce)]\) \(\in\text{trss} f g\) \(\Rightarrow ((s,c),e,(s',c'))\) \(\in\text{trss} f g \land \text{mon-e} f g e \cap \text{mon-e} f g ce = \{\}\)}
\begin{proof}
\end{proof}

\paragraph*{lemma trss-drop-context: \(!!s c. \ [(s,c+ce),w,(s',c'+ce)]\) \(\in\text{trcl} (\text{trss} f g)\) \(\Rightarrow ((s,c),w,(s',c'))\) \(\in\text{trcl} (\text{trss} f g) \land \text{mon-w} f g w \cap \text{mon-e} f g ce = \{\}\)}
\begin{proof}
\end{proof}
\textbf{proof}

\textbf{lemma \textit{trss-xchange-context-s}}:
\begin{itemize}
\item \textbf{assumes} $A: ((s,c),(s',csp+c)) \in \text{trss } fg$
\item \textbf{and} $M: \text{mon-c } fg \cap cn \subseteq \text{mon-c } fg \quad e$
\item \textbf{shows} $((s,-cn),(s',csp+cn)) \in \text{trss } fg$
\end{itemize}
\textbf{proof}

\textbf{lemma \textit{trss-xchange-context}}:
\begin{itemize}
\item \textbf{assumes} $A: ((s,c),(s',csp+c)) \in \text{trcl } (\text{trss } fg)$
\item \textbf{and} $M: \text{mon-c } fg \cap cn \subseteq \text{mon-c } fg \quad e$
\item \textbf{shows} $((s,-cn),(s',csp+cn)) \in \text{trcl } (\text{trss } fg)$
\end{itemize}
\textbf{proof}

\textbf{lemma \textit{trss-drop-all-context-s}}:\textit{cases set, case-names dropped}:
\begin{itemize}
\item \textbf{assumes} $A: ((s,c),(s',c')) \in \text{trss } fg$
\item \textbf{and} $C: !csp. \ [ c' := csp+c; ((s,\{\#\}),e,(s',csp)) \in \text{trss } fg ] \Rightarrow P$
\item \textbf{shows} $P$
\end{itemize}
\textbf{proof}

\textbf{lemma \textit{trss-drop-all-context}}:\textit{cases set, case-names dropped}:
\begin{itemize}
\item \textbf{assumes} $A: ((s,c),w,(s',c')) \in \text{trcl } (\text{trss } fg)$
\item \textbf{and} $C: !csp. \ [ c' := csp+c; ((s,\{\#\}),w,(s',csp)) \in \text{trcl } (\text{trss } fg)] \Rightarrow P$
\item \textbf{shows} $P$
\end{itemize}
\textbf{proof}

\textbf{lemma \textit{tr-add-context-s}}:
\begin{itemize}
\item $[(c,e,c') \in \text{tr } fg; \text{mon-e } fg \quad e \cap \text{mon-c } fg \quad ce = \{\} ] \Rightarrow (c+ce,e,c'+ce) \in \text{tr } fg$
\end{itemize}
\textbf{proof}

\textbf{lemma \textit{tr-add-context}}:
\begin{itemize}
\item $[(c,w,c') \in \text{trcl } (\text{tr } fg); \text{mon-w } fg \quad w \cap \text{mon-c } fg \quad ce = \{\} ] \Rightarrow (c+ce,w,c'+ce) \in \text{trcl } (\text{tr } fg)$
\end{itemize}
\textbf{proof}

\begin{itemize}
\end{itemize}

\section{8 Normalized Paths}

\textbf{theory} \textit{Normalization}

\textbf{imports} \textit{Main ThreadTracking Semantics ConsInterleave}

\begin{itemize}
\end{itemize}

\textbf{begin}

The idea of normalized paths is to consider particular schedules only. While the original semantics allows a context switch to occur after every single step, we now define a semantics that allows context switches only before non-returning calls or after a thread has reached its final stack. We then show that this semantics is able to reach the same set of configurations as
the original semantics.

8.1 Semantic properties of restricted flowgraphs

It makes the formalization smoother, if we assume that every thread’s execution begins with a non-returning call. For this purpose, we defined syntactic restrictions on flowgraphs already (cf. Section 6.3). We now show that these restrictions have the desired semantic effect.

— Procedures with isolated return nodes will never return

\[ \text{lemma (in eflowgraph)} \text{ iso-ret-no-ret: } \forall u. \ [ \begin{array}{l}
  \text{isolated-ret fg p;}
  \text{proc-of fg u = p;}
  u \neq \text{return fg p;}
  (([u], c), w, ([\text{return fg p}], e')) \in \text{trcl (trss fg)}
\end{array} \Rightarrow \text{False} \] 

\( \langle \text{proof} \rangle \)

\[ \text{lemma (in eflowgraph)} \text{ initial-starts-with-call:}
\[ \begin{array}{l}
  \text{\[ (([\text{entry fg p}], c), e, ([s'], c')) \in \text{trss fg; initialproc fg p} \]\]}
  \Rightarrow \exists p', e = \text{LCall p'} \land \text{isolated-ret fg p'}
\] 

\( \langle \text{proof} \rangle \)

\[ \text{lemma (in eflowgraph)} \text{ no-sl-from-initial:}
\[ \text{assumes A: } w \neq \text{[]} \text{ initialproc fg p}
\[ (([\text{entry fg p}], c), w, ([v], c')) \in \text{trcl (trss fg)}
\] 

\text{shows False} 

\( \langle \text{proof} \rangle \)

\[ \text{lemma (in eflowgraph)} \text{ no-retsl-from-initial:}
\[ \text{assumes A: } w \neq \text{[]} \text{ initialproc fg p}
\[ (([\text{entry fg p}], c), w, ([r'], c')) \in \text{trcl (trss fg)}
\] 

\text{length } r' \leq 1 

\text{shows False} 

\( \langle \text{proof} \rangle \)

8.2 Definition of normalized paths

In order to describe the restricted schedules, we define an operational semantics that performs an atomically scheduled sequence of steps in one step, called a macrostep. Context switches may occur after macrosteps only. We call this the normalized semantics and a sequence of macrosteps a normalized path.

Since we ensured that every path starts with a non-returning call, we can define a macrostep as an initial call followed by a same-level path\(^2\) of the

---

\(^2\)Same-level paths are paths with balanced calls and returns. The stack-level at the beginning of their execution is the same as at the end, and during the execution, the stack never falls below the initial level.
called procedure. This has the effect that context switches are either performed before a non-returning call (if the thread makes a further macrostep in the future) or after the thread has reached its final configuration.

As for the original semantics, we first define the normalized semantics on a single thread with a context and then use the theory developed in Section 5 to derive interleaving semantics on multisets and configurations with an explicit local thread (loc/env-semantics, cf. Section 5.4).

**inductive-set**

\[ \text{ntrs} :: (′n,′p,′ba,′m,′more) \text{ flowgraph-rec-scheme} \Rightarrow ((′n \times ′n \ \text{conf}) \times (′p,′ba) \ \text{label list} \times (′n \times ′n \ \text{conf})) \ \text{set} \]

**for fg**

**where**

— A macrostep transforms one thread by first calling a procedure and then doing a same-level path

\[ \text{ntrs-step} : \left[ \left( ((u#r,ce),\text{LCall } p, (\text{entry } fg \ p \ # \ a' \ # \ r,ce)) \in \text{trss } fg; \right. \left. ((\text{entry } fg \ p,ce),w,([v,ce'])\in \text{trcl } (\text{trss } fg)) \right] \Rightarrow ((u#r,ce),\text{LCall } p#w,(v#u'#r,ce'))\in \text{ntrs } fg \]

**abbreviation** \( \text{ntr} \) where \( \text{ntr} \ fg \ = \ gtr (\text{ntrs } fg) \)

**abbreviation** \( \text{ntrp} \) where \( \text{ntrp} \ fg \ = \ gtrp (\text{ntrs } fg) \)

**interpretation** \( \text{ntrs} : \ \text{env-no-step } \text{ntrs } fg \)

\[ \langle \text{proof} \rangle \]

### 8.3 Representation property for reachable configurations

In this section, we show that a configuration is reachable if and only if it is reachable via a normalized path.

The first direction is to show that a normalized path is also a path. This follows from the definitions. Note that we first show that a single macrostep corresponds to a path and then generalize the result to sequences of macrosteps

\[ \text{lemma } \text{ntrs-is-trss-s} : ((s,c),w,(s',c'))\in \text{ntrs } fg \ \Rightarrow ((s,c),w,(s',c'))\in \text{trcl } (\text{trss } fg) \]

\[ \langle \text{proof} \rangle \]

\[ \text{lemma } \text{ntrs-is-trss} : ((s,c),w,(s',c'))\in \text{trcl } (\text{ntrs } fg) \]

\[ \Rightarrow ((s,c),\text{foldl } (\text{op } @) \ [w,(s',c'))\in \text{trcl } (\text{trss } fg) \]

\[ \langle \text{proof} \rangle \]

\[ \text{lemma } \text{ntr-is-tr-s} : (c,w,c')\in \text{ntr } fg \ \Rightarrow (c,w,c')\in \text{trcl } (\text{tr } fg) \]

\[ \langle \text{proof} \rangle \]

\[ \text{lemma } \text{ntr-is-tr} : (c,ww,c')\in \text{trcl } (\text{ntr } fg) \Rightarrow (c,\text{foldl } (\text{op } @) \ [ww,c')\in \text{trcl } (\text{tr } fg) \]

\[ \langle \text{proof} \rangle \]
The other direction requires to prove that for each path reaching a configuration there is also a normalized path reaching the same configuration. We need an auxiliary lemma for this proof, that is a kind of append rule: *Given a normalized path reaching some configuration c, and a same level or returning path from some stack in c, we can derive a normalized path to c modified according to the same-level path.* We cannot simply append the same-level or returning path as a macrostep, because it does not start with a non-returning call. Instead, we will have to append it to some macrostep in the normalized path, i.e. move it “left” into the normalized path.

Intuitively, we can describe the concept of the proof as follows: Due to the restrictions we made on flowgraphs, a same-level or returning path cannot be the first steps on a thread. Hence there is a last macrostep that was executed on the thread. When this macrostep was executed, all threads held less monitors then they do at the end of the execution, because the set of monitors held by every single thread is increasing during the execution of a normalized path. Thus we can append the same-level or returning path to the last macrostep on that thread. As a same-level or returning path does not allocate any monitors, the following macrosteps remain executable. If we have a same-level path, appending it to a macrostep yields a valid macrostep again and we are done. Appending a returning path to a macrostep yields a same-level path. In this case we inductively repeat our argument.

The actual proof is strictly inductive; it either appends the same-level path to the last macrostep or inductively repeats the argument.

**lemma (in eflowgraph) ntr-sl-move-left:** \( !ce \ u \ r \ w \ r' \ ce' \). 
\[
\begin{align*}
(\{\#[\text{entry } fg \ p]\#\}, ww, \{\# u \# r \# \}+ce) & \in trcl (ntr fg); \\
([u, ce], w, (r', ce')) & \in trcl (trss fg); \\
\text{initialproc } fg \ p; \\
\text{length } r' \leq 1; w \neq [] \\
\Rightarrow \exists \, ww'. (\{\#[\text{entry } fg \ p]\#\}, ww', \{\# r'@r \# \}+ce') & \in trcl (ntr fg)
\end{align*}
\]

Finally we can prove: *Any reachable configuration can also be reached by a normalized path.* With eflowgraph.ntr-sl-move-left we can easily show this lemma With eflowgraph.ntr-sl-move-left we can easily show this by induction on the reaching path. For the empty path, the proposition follows trivially. Else we consider the last step. If it is a call, we can execute it as a macrostep and get the proposition. Otherwise the last step is a same-level (Base, Spawn) or returning (Ret) path of length 1, and we can append it to the normalized path using eflowgraph.ntr-sl-move-left.

**lemma (in eflowgraph) normalize:** 
\[
\begin{align*}
(cstart, w, c') & \in trcl (tr fg); \\
cstart = \{\#[\text{entry } fg \ p]\#\}; \\
\text{initialproc } fg \ p \\
\Rightarrow \exists \, w'. (\{\#[\text{entry } fg \ p]\#\}, w', c') & \in trcl (ntr fg)
\end{align*}
\]
— The lemma is shown by induction on the reaching path
⟨proof⟩

As the main result of this section we get: A configuration is reachable if and only if it is also reachable via a normalized path:

**Theorem** (in flowgraph) **ntr-repr**:

\[(\exists w. \left\{ \#\text{entry fg (main fg)}\right\}, w, c) \in \text{trcl} (\text{tr fg}) \quad \iff \quad (\exists w. \left\{ \#\text{entry fg (main fg)}\right\}, w, c) \in \text{trcl} (\text{ntr fg})\]

⟨proof⟩

8.4 Properties of normalized path

Like a usual path, also a macrostep modifies one thread, spawns some threads and preserves the state of all the other threads. The spawned threads do not make any steps, thus they stay in their initial configurations.

**Lemma** **ntrs-c-cases-s**[
\[
\exists w. \left\{ \#\text{entry fg (main fg)}\right\}, w, c \quad \forall w. \left\{ \#\text{csp} = c + c\right\} \quad \exists p u v. \left( (u, \text{Spawn} p, v) \in \text{edges fg} \quad \text{initialproc fg p} \right) \]
\] \implies \quad P
\]
⟨proof⟩

**Lemma** **ntrs-c-cases**[
\[
\exists w. \left\{ \#\text{entry fg (main fg)}\right\}, w, c \quad \forall w. \left\{ \#\text{csp} = c + c\right\} \quad \exists p u v. \left( (u, \text{Spawn} p, v) \in \text{edges fg} \quad \text{initialproc fg p} \right) \]
\] \implies \quad P
\]
⟨proof⟩

8.4.1 Validity

Like usual paths, also normalized paths preserve validity of the configurations.

**Lemmas** (in flowgraph) **ntrp-valid-preserve**[
\[
\exists \left\{ \#\text{entry fg (main fg)}\right\}, w, c \quad \forall w. \left\{ \#\text{csp} = c + c\right\} \quad \exists p u v. \left( (u, \text{Spawn} p, v) \in \text{edges fg} \quad \text{initialproc fg p} \right) \]
\] \implies \quad P
\]
⟨proof⟩

**Lemma** (in flowgraph) **ntrp-valid-preserve**:

\[
\exists \left\{ \#\text{entry fg (main fg)}\right\}, w, c \quad \forall w. \left\{ \#\text{csp} = c + c\right\} \quad \exists p u v. \left( (u, \text{Spawn} p, v) \in \text{edges fg} \quad \text{initialproc fg p} \right) \]
\] \implies \quad P
\]
⟨proof⟩

**Lemma** (in flowgraph) **ntrp-valid-preserve**:
assumes $A: ((s,c), e, (s', c')) \in \text{trcl} (\text{ntrp} fg)$

and $V: \text{valid} fg (\{\#s\#\} + c)$

shows $\text{valid} fg (\{\#s'\#\} + c')$

\langle proof \rangle

### 8.4.2 Monitors

The following defines the set of monitors used by a normalized path and shows its basic properties:

**definition**

$\text{mon-ww fg} \; \text{ww} \; == \; \text{foldl} (\text{op} \cup) \{\} (\text{map} (\text{mon-w fg}) \; \text{ww})$

**definition**

$\text{mon-loc fg} \; \text{ww} \; == \; \text{mon-ww fg} (\text{map le-rem-s} (\text{loc} \; \text{ww}))$

**definition**

$\text{mon-env fg} \; \text{ww} \; == \; \text{mon-ww fg} (\text{map le-rem-s} (\text{env} \; \text{ww}))$

**lemma** $\text{mon-ww-empty} [\text{simp}]$: $\text{mon-ww fg} \; [] = \{\}$

\langle proof \rangle

**lemma** $\text{mon-ww-uncons} [\text{simp}]$:

$\text{mon-ww fg} (\text{ee} \# \text{ww}) = \text{mon-w fg} \; \text{ee} \cup \text{mon-ww fg} \; \text{ww}$

\langle proof \rangle

**lemma** $\text{mon-ww-unconc}$:

$\text{mon-ww fg} (\text{ww1} @ \text{ww2}) = \text{mon-ww fg} \; \text{ww1} \cup \text{mon-ww fg} \; \text{ww2}$

\langle proof \rangle

**lemma** $\text{mon-env-empty} [\text{simp}]$: $\text{mon-env fg} \; [] = \{\}$

\langle proof \rangle

**lemma** $\text{mon-env-single} [\text{simp}]$:

$\text{mon-env fg} \; [e] = (\text{case e of LOC a} \Rightarrow \{\} | \text{ENV a} \Rightarrow \text{mon-w fg} \; a)$

\langle proof \rangle

**lemma** $\text{mon-env-uncons} [\text{simp}]$:

$\text{mon-env fg} (\text{e#w}) = (\text{case e of LOC a} \Rightarrow \{\} | \text{ENV a} \Rightarrow \text{mon-w fg} \; a) \cup \text{mon-env fg w}$

\langle proof \rangle

**lemma** $\text{mon-env-unconc}$:

$\text{mon-env fg} (\text{w1}@\text{w2}) = \text{mon-env fg} \; \text{w1} \cup \text{mon-env fg} \; \text{w2}$

\langle proof \rangle

**lemma** $\text{mon-loc-empty} [\text{simp}]$: $\text{mon-loc fg} \; [] = \{\}$

\langle proof \rangle

**lemma** $\text{mon-loc-single} [\text{simp}]$:

$\text{mon-loc fg} \; [e] = (\text{case e of ENV a} \Rightarrow \{\} | \text{LOC a} \Rightarrow \text{mon-w fg} \; a)$

\langle proof \rangle

**lemma** $\text{mon-loc-uncons} [\text{simp}]$:

$\text{mon-loc fg} (\text{e#w}) = (\text{case e of ENV a} \Rightarrow \{\} | \text{LOC a} \Rightarrow \text{mon-w fg} \; a) \cup \text{mon-loc fg w}$

\langle proof \rangle
lemma mon-loc-unconc:
\[ \text{mon-loc } fg (w_1 \otimes w_2) = \text{mon-loc } fg w_1 \cup \text{mon-loc } fg w_2 \]

lemma mon-ww-of-foldl[simp]: \[ \text{mon-w } fg (\text{foldl } (\text{op } \#) [] \text{ww}) = \text{mon-ww } fg \text{ww} \]

lemma mon-ww-ileq: \[ w \preceq w' \implies \text{mon-ww } fg w \subseteq \text{mon-ww } fg w' \]

lemma mon-ww-cil:
\[ w \in w_1 \otimes_\alpha w_2 \implies \text{mon-ww } fg w = \text{mon-ww } fg w_1 \cup \text{mon-ww } fg w_2 \]

lemma mon-loc-cil:
\[ w \in w_1 \otimes_\alpha w_2 \implies \text{mon-loc } fg w = \text{mon-loc } fg w_1 \cup \text{mon-loc } fg w_2 \]

lemma mon-env-cil:
\[ w \in w_1 \otimes_\alpha w_2 \implies \text{mon-env } fg w = \text{mon-env } fg w_1 \cup \text{mon-env } fg w_2 \]

lemma mon-ww-of-le-rem:
\[ \text{mon-ww } fg (\text{map } \text{le-rem-s } w) = \text{mon-loc } fg w \cup \text{mon-env } fg w \]

lemma mon-env-ileq:
\[ w \preceq w' \implies \text{mon-env } fg w \subseteq \text{mon-env } fg w' \]

lemma mon-loc-ileq:
\[ w \preceq w' \implies \text{mon-loc } fg w \subseteq \text{mon-loc } fg w' \]

lemma mon-loc-map-loc[simp]: \[ \text{mon-loc } fg (\text{map } \text{LOC } w) = \text{mon-ww } fg w \]

lemma mon-env-map-env[simp]: \[ \text{mon-env } fg (\text{map } \text{ENV } w) = \text{mon-ww } fg w \]

lemma mon-loc-map-env[simp]: \[ \text{mon-loc } fg (\text{map } \text{ENV } w) = \{\} \]

lemma mon-env-map-loc[simp]: \[ \text{mon-env } fg (\text{map } \text{LOC } w) = \{\} \]

lemma (in flowgraph) ntr-mon-increasing-s: \[ ((s,c),e,(s',c')) \in \text{ntr } fg \]
\[ \implies \text{mon } s \text{fg } s \subseteq \text{mon } s \text{fg } s' \wedge \text{mon } c \text{fg } c = \text{mon } c \text{fg } c' \]

lemma (in flowgraph) ntrp-mon-increasing-s:
\[ (c,ce,c') \in \text{ntrp } fg \implies \text{mon } c \text{fg } c \subseteq \text{mon } c \text{fg } c' \]
\[ \text{mon-s fg } s \subseteq \text{mon-s fg } s' \land \text{mon-c fg } c \subseteq \text{mon-c fg } c' \]

\[ \langle \text{proof} \rangle \]

**Lemma (in flowgraph) ntrp-mon-increasing:** \((s,c), e, (s',c') \in \text{trcl (ntrp fg)}\)
\[ \Rightarrow \text{mon-s fg } s \subseteq \text{mon-s fg } s' \land \text{mon-c fg } c \subseteq \text{mon-c fg } c' \]

\[ \langle \text{proof} \rangle \]

**8.4.3 Modifying the context**

**Lemmas (in flowgraph) ntrs-c-no-mon-s = trss-c-no-mon[OF ntrs-is-trss-s]**

**Lemmas (in flowgraph) ntrs-c-no-mon = trss-c-no-mon[OF ntrs-is-trss]**

Also like a usual path, a normalized step must not use any monitors that are allocated by other threads

**Lemmas (in flowgraph) ntrs-mon-e-no-ctx = trss-mon-w-no-ctx[OF ntrs-is-trss-s]**

**Lemma (in flowgraph) ntrs-mon-e-no-ctx:**
\[ \text{assumes } A: ((s,c),w,(s',c')) \in \text{trcl (ntrp fg)} \]
\[ \text{shows } \text{mon-ww fg } w \cap \text{mon-c fg } c = \{ \} \]

\[ \langle \text{proof} \rangle \]

**Lemma (in flowgraph) ntrs-mon-loc-e-no-ctx:**
\[ ((s,c),w,(s',c')) \in \text{trcl (ntrp fg)} \Rightarrow \text{mon-loc fg } w \cap \text{mon-c fg } c = \{ \} \]

\[ \langle \text{proof} \rangle \]

The next lemmas are rules how to add or remove threads while preserving the executability of a path

**Lemma (in flowgraph) ntrs-modify-context-s:**
\[ \text{assumes } A: ((s,c),e,(s',c')) \in \text{ntrp fg} \]
\[ \text{and } B: \text{mon-w fg } ee \cap \text{mon-c fg } cn = \{ \} \]
\[ \text{shows } \exists \text{csp. } c' = \text{csp+c } \land \text{mon-c fg } cspо = \{ \} \land ((s,cn),e,(s',csp+cn)) \in \text{ntrp fg} \]

\[ \langle \text{proof} \rangle \]

**Lemma (in flowgraph) ntrs-modify-context[rule-format]:**
\[ \text{∃ cn. } \text{mon-ww fg } w \cap \text{mon-c fg } cn = \{ \} \]
\[ \Rightarrow (\exists \text{csp. } c' = \text{csp+c } \land \text{mon-c fg } cspо = \{ \} \land ((s,cn),w,(s',csp+cn)) \in \text{trcl (ntrp fg)}) \]
\langle proof \rangle

**Lemma ntrs-xchange-context-s:**
- **Assumes** A: ((s, c), ee, (s', csp + c)) \(\in\) ntrs fg
- **And** B: mon-c fg cn \(\subseteq\) mon-c fg c
- **Shows** ((s, cn), ee, (s', csp + cn)) \(\in\) ntrs fg

\langle proof \rangle

**Lemma ntrs-replace-context-s:**
- **Assumes** A: ((s, c + cr), ee, (s', c' + cr)) \(\in\) ntrs fg
- **And** B: mon-c fg crn \(\subseteq\) mon-c fg cr
- **Shows** ((s, c + crn), ee, (s', c' + crn)) \(\in\) ntrs fg

\langle proof \rangle

**Lemma (in flowgraph) ntrs-xchange-context:** \(!! s \ c' \ cn. \ [\]
- ((s, c), w, (s', c')) \(\in\) trcl (ntrs fg);
- mon-c fg cn \(\subseteq\) mon-c fg c
- \[\] \(\Rightarrow\) \exists csp.

\langle proof \rangle

**Lemma (in flowgraph) ntrs-replace-context:**
- **Assumes** A: ((s, c + cr), w, (s', c' + cr)) \(\in\) trcl (ntrs fg)
- **And** B: mon-c fg crn \(\subseteq\) mon-c fg cr
- **Shows** ((s, c + crn), w, (s', c' + crn)) \(\in\) trcl (ntrs fg)

\langle proof \rangle

**Lemma (in flowgraph) ntr-add-context-s:**
- **Assumes** A: (c, e, c') \(\in\) ntr fg
- **And** B: mon-w fg e \(\cap\) mon-c fg cn = \{\}
- **Shows** (c + cn, e, c' + cn) \(\in\) ntr fg

\langle proof \rangle

**Lemma (in flowgraph) ntrp-add-context-s:**
- **Assumes** A: ((s, c), e, (s', c')) \(\in\) ntrp fg
- **And** B: mon-w fg e \(\cap\) mon-c fg cn = \{\}
- **Shows** ((s, c + cn), e, (s', c' + cn)) \(\in\) ntrp fg

\langle proof \rangle

**Lemma (in flowgraph) ntrs-add-context-s:**
- **Assumes** A: ((s, c), ee, (s', csp + e)) \(\in\) ntrs fg
- **And** B: mon-c fg cn \(\subseteq\) mon-c fg e
- **Shows** ((s, cn), ee, (s', csp + cn)) \(\in\) ntrs fg

\langle proof \rangle
lemma (in flowgraph) ntrp-add-context: [ \[(s,c),w,(s',c')\)\in\text{trcl}(\text{ntrp fg});
\ mon-ww fg (map le-rem-s w) \cap mon-c fg cn = \{\}\]
\[\implies ((s,c+cn),w,(s',c'+cn))\in\text{trcl}(\text{ntrp fg})\]

8.4.4 Altering the local stack

lemma ntrs-stack-comp-s:
assumes A: \[(s,c),ee,(s',c')\)\in\text{ntrs fg}\]
shows \[(s@r,c),ee,(s@r,c')\)\in\text{ntrs fg}\]

lemma ntrs-stack-comp: \[(s,c),ww,(s',c')\)\in\text{trcl}(\text{ntrs fg})\]
\implies \[(s@r,c),ww,(s@r,c')\)\in\text{trcl}(\text{ntrs fg})\]

lemma (in flowgraph) ntrp-stack-comp-s:
assumes A: \[(s,c),ee,(s',c')\)\in\text{ntrp fg}\]
and B: mon-s fg r \cap mon-env fg [ee] = \{\}\]
shows \[(s@r,c),ee,(s@r,c')\)\in\text{ntrs fg}\]

lemma (in flowgraph) ntrp-stack-comp:
\[\text{[ (s,c),ww,(s',c')\)\in\text{trcl}(\text{ntrp fg}); mon-s fg r \cap mon-env fg ww = \{\}\] 
\implies \[(s@r,c),ww,(s@r,c')\)\in\text{trcl}(\text{ntrs fg})\]

lemma ntrs-stack-top-decomp-s:
assumes A: \[(u@r,c),ee,(s',c')\)\in\text{ntrs fg}\]
and EX: !!v u'. [ \[s'=v\#u'\#r; \[(u@r,c),ee,(v,u',c')\)\in\text{ntrs fg};
\uCall p,u'\)\in\text{edges fg}\]
\] \implies P
shows P

lemma ntrs-stack-decomp-s:
assumes A: \[(u@r,c),ee,(s',c')\)\in\text{ntrs fg}\]
and EX: !!v u'. [ \[s'=v\#u'\#s@r; \[(u@r,c),ee,(v\#u'\#s,c')\)\in\text{ntrs fg};
\uCall p,u'\)\in\text{edges fg}\]
\] \implies P
shows P

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lemma ntrs-stack-decomp: \( \forall u \, s \, r \, c \, P. \] 
\( ((u \# s @ r, c), \{(s', c')\}) \in \text{trcl} (\text{ntrs fg}); \) 
\( \forall v \, r. \] 
\( [s'=v\# r @ r; ((u \# s, c), \{(v\# r, c')\}) \in \text{trcl} (\text{ntrs fg})] \implies P \) 
\( \] 
\( \] 
\hfill \) 

lemma ntrp-stack-decomp-s: \hfill \] 
assumes \( A: ((u \# s @ r, c), \{(s', c')\}) \in \text{ntrp fg} \) 
and \( EX: \forall v \, r. \] 
\( [s'=v\# r @ r; ((u \# s, c), \{(v\# r, c')\}) \in \text{ntrp fg}] \implies P \) 
shows \( P \] 
\hfill \) 

\hfill \) 

8.5 Relation to monitor consistent interleaving

In this section, we describe the relation of the consistent interleaving operator (cf. Section 2) and the macrostep-semantics.

8.5.1 Abstraction function for normalized paths

We first need to define an abstraction function that maps a macrostep on a pair of entered and passed monitors, as required by the \( \otimes \alpha \)-operator:

A step on a normalized paths enters the monitors of the first called procedure and passes the monitors that occur in the following same-level path.

definition \( \alpha n fg e == \) if \( e=\[] \) then \( (\{\}, \{\}) \) else \( (\text{mon-e fg (hd e)}, \text{mon-w fg (tl e)}) \)

lemma \( \alpha n-simps[simp]: \] 
\( \alpha n fg [] = (\{\}, \{\}) \) 
\( \alpha n fg (e\# w) = (\text{mon-e fg e}, \text{mon-w fg w}) \) 
\[ proof \] 

definition \( \alpha nl fg e == \alpha n fg (\text{le-rem-s e}) \)

lemma \( \alpha nl-def': \alpha nl fg == \alpha n fg \circ \text{le-rem-s} \) 
\[ proof \] 

lemma \( \alpha nl-simps[simp]: \) 
\( \alpha nl fg (\text{ENV x}) = \alpha n fg x \) 
\( \alpha nl fg (\text{LOC x}) = \alpha n fg x \) 
\[ proof \] 

lemma \( \alpha nl-simps1[simp]: \)
\[(\alpha n fg) \circ ENV = \alpha n fg\]
\[(\alpha n fg) \circ LOC = \alpha n fg\]
\[
\text{lemma } \alpha n\alpha n: (\alpha n fg) \circ \text{le-rem-s} = \alpha n fg
\]
\[
\text{lemma } \alpha n\text{-fst-snd}[\text{simp}]: \text{fst}(\alpha n fg w) \cup \text{snd}(\alpha n fg w) = \text{mon-w fg w}
\]
\[
\text{lemma } \text{mon-pl-of-}\alpha n\text{-}\alpha n: \text{mon-pl}(\text{map}(\alpha n fg) w) = \text{mon-w w fg w}
\]
We now derive specialized introduction lemmas for \(\otimes\alpha n fg\)
\[
\text{lemma } \text{cil-}\alpha n\text{-cons-helper}: \text{mon-pl}(\text{map}(\alpha n fg) wb) = \text{mon-w w fg wb}
\]
\[
\text{lemma } \text{cil-}\alpha n\text{-cons-helper}:
\quad \text{mon-pl}(\text{map}(\alpha n fg) wb) = \text{mon-w w fg (map le-rem-s wb)}
\]
\[
\text{lemma } \text{cil-}\alpha n\text{-cons1}: [w \in \alpha n \otimes \alpha n fg wb; \text{fst}(\alpha n fg e) \cap \text{mon-w w fg wb} = \{\}]
\quad \Rightarrow e\#w \in e\#w \otimes \alpha n fg wb
\]
\[
\text{lemma } \text{cil-}\alpha n\text{-cons2}: [w \in \alpha n \otimes \alpha n fg wb; \text{fst}(\alpha n fg e) \cap \text{mon-w w fg wa} = \{\}]
\quad \Rightarrow e\#w \in w \otimes \alpha n fg e\#wb
\]

8.5.2 Monitors

\[
\text{lemma (in flowgraph) ntrs-mon-s:}
\quad \text{assumes } A: ((s,c),e,(s',c')) \in \text{ntrs fg}
\quad \text{shows } \text{mon-s fg s} = \text{mon-s fg s} \cup \text{fst(\alpha n fg e)}
\]
\[
\text{corollary (in flowgraph) ntrs-called-mon:}
\quad \text{assumes } A: ((s,c),e,(s',c')) \in \text{ntrs fg}
\quad \text{shows } \text{fst}(\alpha n fg e) \subseteq \text{mon-s fg s} \cup \text{fst(\alpha n fg e)}
\]
\[
\text{lemma (in flowgraph) ntr-mon-s:}
\quad (c,e,c') \in \text{ntr fg} \implies \text{mon-c fg c'} = \text{mon-c fg c} \cup \text{fst(\alpha n fg e)}
\]
\[
\text{lemma (in flowgraph) ntrp-mon-s:}
\quad \text{assumes } A: ((s,c),e,(s',c')) \in \text{ntrp fg}
\quad \text{shows } \text{mon-c fg (\{\#s'\#\} + c')} = \text{mon-c fg (\{\#s\#\} + c) \cup \text{fst(\alpha n lg e)}}
\]
8.5.3 Interleaving theorem

In this section, we show that the consistent interleaving operator describes the intuition behind interleavability of normalized paths. We show: Two paths are simultaneously executable if and only if they are consistently interleavable and the monitors of the initial configurations are compatible.

The split lemma splits an execution from a context of the form \( ca + cb \) into two interleavable executions from \( ca \) and \( cb \) respectively. While further down we prove this lemma for loc/env-path, which is more general but also more complicated, we start with the proof for paths of the multiset-semantics for illustrating the idea.

**lemma (in flowgraph) ntr-split:**

\[
\forall ca \; cb. \; [(ca + cb, w, c') \in \text{trcl} (ntr fg); \; \text{valid} \; fg \; (ca + cb)] \implies \\
\exists ca' \; cb' \; wa \; wb. \;
\begin{align*}
&c' = ca' + cb' \land \\
&w \in (wa \otimes \alpha_n fg \; wb) \land \\
&\text{mon-c} \; fg \; ca \cap (\text{mon-c} \; fg \; cb \cup \text{mon-ww} \; fg \; wb) = \{\} \land \\
&\text{mon-c} \; fg \; cb \cap (\text{mon-c} \; fg \; ca \cup \text{mon-ww} \; fg \; wa) = \{\} \land \\
&(ca, wa, ca') \in \text{trcl} (ntr fg) \land (cb, wb, cb') \in \text{trcl} (ntr fg)
\end{align*}
\]

⟨proof⟩

The next lemma is a more general version of flowgraph.ntr-split for the semantics with a distinguished local thread. The proof follows exactly the same ideas, but is more complex.

**lemma (in flowgraph) ntrp-split:**

\[
\forall s \; c1 \; c2 \; s' \; c'. \; [(s, c1 + c2), w, (s', c')] \in \text{trcl} (ntrp fg); \; \text{valid} \; fg \; (\{#s#\} + c1 + c2)] \implies \\
\exists w1 \; w2 \; c1' \; c2'. \\
\begin{align*}
&w \in w1 \otimes \alpha_nl fg \; (\text{map ENV} \; w2) \land \\
&c' = c1' + c2' \land \\
&((s, c1), w1, (s', c1')) \in \text{trcl} (ntrp fg) \land \\
&(c2, w2, c2') \in \text{trcl} (ntrp fg) \land \\
&\text{mon-ww} \; fg \; (\text{map le-rem-s} \; w1) \cap \text{mon-c} \; fg \; c2 = \{\} \land \\
&\text{mon-ww} \; fg \; w2 \cap \text{mon-c} \; fg \; (\{#s#\} + c1) = \{\}
\end{align*}
\]

⟨proof⟩

**lemma (in flowgraph) ntr-split':**

**assumes** \( A: \; (ca + cb, w, c') \in \text{trcl} (ntr fg) \)

**and** \( \text{VALID}: \; \text{valid} \; fg \; (ca + cb) \)

**shows** \( \exists ca' \; cb' \; wa \; wb. \;
\begin{align*}
&c' = ca' + cb' \land \\
&w \in (wa \otimes \alpha_n fg \; wb) \land \\
&\text{mon-c} \; fg \; ca \cap (\text{mon-c} \; fg \; cb \cup \text{mon-ww} \; fg \; wb) = \{\} \land \\
&\text{mon-c} \; fg \; cb \cap (\text{mon-c} \; fg \; ca \cup \text{mon-ww} \; fg \; wa) = \{\} \land \\
&(ca, wa, ca') \in \text{trcl} (ntr fg) \land (cb, wb, cb') \in \text{trcl} (ntr fg)
\end{align*}
\]

⟨proof⟩
The unsplit lemma combines two interleavable executions. For illustration purposes, we first prove the less general version for multiset-configurations. The general version for loc/env-configurations is shown later.

**Lemma (in flowgraph) ntr-unsplit:**

assumes: \(A: w \in wa \otimes_{an} fg \land wb\)

\(B: (ca, wa, ca') \in \text{trcl} (ntr fg)\)

\((cb, wb, cb') \in \text{trcl} (ntr fg)\)

\(\text{mon-c fg ca} \cap (\text{mon-c fg cb} \cup \text{mon-ww fg wb}) = \{}\)

\(\text{mon-c fg cb} \cap (\text{mon-c fg ca} \cup \text{mon-ww fg wa}) = \{}\)

shows \((ca + cb, w, ca' + cb') \in \text{trcl} (ntr fg)\)

(proof)

**Lemma (in flowgraph) ntrp-unsplit:**

assumes: \(A: w \in wa \otimes_{anl} fg\)

\(B: ((s, ca), wa, (s', ca')) \in \text{trcl} (ntrp fg)\)

\((cb, wb, cb') \in \text{trcl} (ntrp fg)\)

\(\text{mon-c fg} ((\#s\#) + ca) \cap (\text{mon-c fg cb} \cup \text{mon-ww fg wb}) = \{}\)

\(\text{mon-c fg cb} \cap (\text{mon-c fg} ((\#s\#) + ca) \cup \text{mon-ww fg wa}) = \{}\)

shows \(((s, ca + cb), w, (s', ca' + cb')) \in \text{trcl} (ntrp fg)\)

(proof)

And finally we get the desired theorem: *Two paths are simultaneously executable if and only if they are consistently interleavable and the monitors of the initial configurations are compatible.* Note that we have to assume a valid starting configuration.

**Theorem (in flowgraph) ntr-interleave:**

valid \(fg\) \((ca + cb) \implies \exists ca' \in \text{trcl} (ntrp fg) \implies \exists ca' cb' \in \text{trcl} (ntrp fg) \land \exists ca'

\((ca + cb, w, c') \in \text{trcl} (ntrp fg) \implies \exists ca' \in \text{trcl} (ntrp fg) \land \exists ca' cb' \in \text{trcl} (ntrp fg) \land \exists ca'

\((\#s\#) + c1 + c2) \implies (\#s\#) + c1 + c2'\)

\(w \in w1 \otimes_{anl} fg\)

\((s, c1 + c2), w, (s', c') \in \text{trcl} (ntrp fg) \implies (s, c1 + c2), w, (s', c') \in \text{trcl} (ntrp fg) \land (c1 + c2 + c2') \in \text{trcl} (ntrp fg) \land \exists w1 \otimes_{anl} fg\)

\((w, w1 (s', c') \in \text{trcl} (ntrp fg) \land (c2, w2, c2') \in \text{trcl} (ntrp fg) \land \exists w1 \otimes_{anl} fg\)

\((s, c1), w1 (s', c') \in \text{trcl} (ntrp fg) \land (c2, w2, c2') \in \text{trcl} (ntrp fg) \land \exists w1 \otimes_{anl} fg\)

\(\text{mon-ww fg} \land \text{mon-ww fg} (\#s\# + c1) = \{}\)

(proof)
The next is a corollary of flowgraph \textit{ntrp-unsplit}, allowing us to convert a path to loc/env semantics by adding a local stack that does not make any steps.

**Corollary** (in flowgraph) \textit{ntr2ntrp}: \[
\begin{align*}
(c, w, c') & \in \text{trcl} (\text{ntr} f g); \\
\text{mon-c} f g (\{\#s\#\} + c) \cap (\text{mon-c} f g c \cup \text{mon-ww} f g w) = \emptyset \\
\Rightarrow ((s, c l + c), \text{map ENV} w, (s, c l + c')) \in \text{trcl} (\text{ntrp} f g)
\end{align*}
\]

8.5.4 Reverse splitting

This section establishes a theorem that allows us to find the thread in the original configuration that created some distinguished thread in the final configuration.

**Lemma** (in flowgraph) \textit{ntr-reverse-split}: \[
\begin{align*}
(c, w, \{\#s\#\} + c e') & \in \text{trcl} (\text{ntr} f g); \\
\text{valid} f g c & \Rightarrow \\
\exists s c e w1 w2 c e1' c e2'.
\end{align*}
\]

\[
\begin{align*}
c & = \{\#s\#\} + c e \land \\
(c e' & = c e1' + c e2' \land \\
 w & \in w1 \otimes_{\alpha f g} w2 \land \\
\text{mon-s} f g s & \cap (\text{mon-c} f g c e \cup \text{mon-ww} f g w) = \emptyset \land \\
\text{mon-c} f g c e & \cap (\text{mon-s} f g s \cup \text{mon-ww} f g w) = \emptyset \land \\
\{(\#s\#), w1, \{\#s\#\} + c e1'\} & \in \text{trcl} (\text{ntr} f g) \land \\
(c e, w2, c e2') & \in \text{trcl} (\text{ntr} f g)
\end{align*}
\]

— The proof works by induction on the initial configuration. Note that configurations consist of finitely many threads only

— FIXME: An induction over the size (rather than over the adding of some fixed element) may lead to a smoother proof here

\begin{proof}
\end{proof}

9 Constraint Systems

**Theory** ConstraintSystems

**Imports** Main AcquisitionHistory Normalization

**Begin**

In this section we develop a constraint-system-based characterization of our analysis.

Constraint systems are widely used in static program analysis. There least solution describes the desired analysis information. In its generic form, a constraint system \( R \) is a set of inequations over a complete lattice \((L, \sqsubseteq)\) and a set of variables \( V \). An inequation has the form \( R[v] \sqsubseteq \text{rhs} \), where \( R[v] \in V \) and \( \text{rhs} \) is a monotonic function over the variables. Note that
for program analysis, there is usually one variable per control point. The variables are then named $R[v]$, where $v$ is a control point. By standard fixed-point theory, those constraint systems have a least solution. Outside the constraint system definition $R[v]$ usually refers to a component of that least solution.

Usually a constraint system is generated from the program. For example, a constraint generation pattern could be the following:

$$\text{for } (u, \text{Call } q, v) \in E:
\quad S^k[v] \supseteq \{(\text{mon}(q) \cup M \cup M', \hat{P}) \mid (M, P) \in S^k[u] \land (M', P') \in S^k[r_q] \\
\land \hat{P} \leq P \uplus P' \land |\hat{P}| \leq 2\}$$

For some parameter $k$ and a flowgraph with nodes $N$ and edges $E$, this generates a constraint system over the variables $\{S^k[v] \mid v \in N\}$. One constraint is generated for each call edge. While we use a powerset lattice here, we can in general use any complete lattice. However, all the constraint systems needed for our conflict analysis are defined over powerset lattices $(P'(a), \subseteq)$ for some type $'a$. This admits a convenient formalization in Isabelle/HOL using inductively defined sets. We inductively define a relation between variables\(^3\) and the elements of their values in the least solution, i.e. the set $\{(v, x) \mid x \in R[v]\}$. For example, the constraint generator pattern from above would become the following introduction rule in the inductive definition of the set $S\text{-cs } fg \; k$:

$$\left[(u, \text{Call } q, v) \in \text{edges } fg;\; (u, M, P) \in S\text{-cs } fg \; k;\; \left(\text{return } fg \; q, M_s, P_s\right) \in S\text{-cs } fg \; k;\; P' \leq \#P + P_s;\; \text{size } P' \leq k \right] \implies (v, \text{mon } fg \; q \cup M \cup M_s, P') \in S\text{-cs } fg \; k$$

The main advantage of this approach is that one gets a concise formalization by using Isabelle’s standard machinery, the main disadvantage is that this approach only works for powerset lattices ordered by $\subseteq$.

### 9.1 Same-level paths

#### 9.1.1 Definition

We define a constraint system that collects abstract information about same-level paths. In particular, we collect the set of used monitors and all multi-subsets of spawned threads that are not bigger than $k$ elements, where $k$ is a parameter that can be freely chosen.

An element $(u, M, P) \in S\text{-cs } fg \; k$ means that there is a same-level path from the entry node of the procedure of $u$ to $u$, that uses the monitors $M$ and spawns at least the threads in $P$.

---

\(^3\)Variables are identified by control nodes here
The intuition underlying this constraint system is the following: The \textit{S-init}-constraint describes that the procedures entry node can be reached with the empty path, that has no monitors and spawns no procedures. The \textit{S-base}-constraint describes that executing a base edge does not use monitors or spawn threads, so each path reaching the start node of the base edge also induces a path reaching the end node of the base edge with the same set of monitors and the same set of spawned threads. The \textit{S-call}-constraint models the effect of a procedure call. If there is a path to the start node of a call edge and a same-level path through the procedure, this also induces a path to the end node of the call edge. This path uses the monitors of both path and spawns the threads that are spawned on both paths. Since we only record a limited subset of the spawned threads, we have to choose which of the threads are recorded. The \textit{S-spawn}-constraint models the effect of a spawn edge. A path to the start node of the spawn edge induces a path to the end node that uses the same set of monitors and spawns the threads of the initial path plus the one spawned by the spawn edge. We again have to choose which of these threads are recorded.

\subsection{Soundness and Precision}

Soundness of the constraint system \textit{S-cs} means, that every same-level path has a corresponding entry in the constraint system.

As usual the soundness proof works by induction over the length of execution paths. The base case (empty path) trivially follows from the \textit{S-init} constraint. In the inductive case, we consider the edge that induces the last step of the path; for a return step, this is the corresponding call edge (cf. Lemma flowgraph.trss-find-call'). With the induction hypothesis, we get the soundness for the (shorter) prefix of the path, and depending on the last step we can choose a constraint that implies soundness for the whole path.

\textbf{lemma (in flowgraph)} \textit{S-sound}: \textit{!!p v c' P}. 
being represented by some path in the operational characterization. For proving precision, one usually shows that a family of sets derived as an abstraction from the operational characterization solves all constraints. In our formalization of constraint systems as inductive sets this amounts to constructing for each constraint a justifying path for the entries described on the conclusion side of the implication – under the assumption that corresponding paths exists for the entries mentioned in the antecedent.

**Lemma (in flowgraph) S-precise**: \( (v,M,P) \in S-cs \mathbf{fg} k \)

\[
\implies \exists p c' w. (\langle [\text{entry fg } p],[\#]\rangle,w,[[v],c']) \in \text{trcl} (\text{trss fg}) \land \\
\text{size } P \leq k \land \\
(\lambda p. [\text{entry fg } p]) \cdot \# P \leq \# c' \land \\
M = \text{mon-w } \mathbf{fg} w
\]

**Proof**

**Theorem (in flowgraph) S-sound-precise**: \( (v,M,P) \in S-cs \mathbf{fg} k \iff \) \( (\exists p c' w. (\langle [\text{entry fg } p],[\#]\rangle,w,[[v],c']) \in \text{trcl} (\text{trss fg}) \land \\
\text{size } P \leq k \land \\
(\lambda p. [\text{entry fg } p]) \cdot \# P \leq \# c' \land \\
M = \text{mon-w } \mathbf{fg} w
\)

Next, we present specialized soundness and precision lemmas, that reason over a macrostep \((\mathbf{ntrp fg})\) rather than a same-level path \((\text{trcl} (\text{trss fg}))\). They are tailored for the use in the soundness and precision proofs of the other constraint systems.

**Lemma (in flowgraph) S-sound-ntrp**: \( (v,M,P) \in S-cs \mathbf{fg} k \)

**Assumes A**: \( (\langle [u],[\#]\rangle,\langle \ell e, (\langle sh, ch \rangle) \rangle) \in \mathbf{ntrp fg} \) and

**CASE**: \( !! \ell e = \ell \text{OC} \) \( (\ell \text{Call } p \# u \#) \); \( (u, \ell \text{Call } p, u') \in \text{edges } \mathbf{fg} \); \( sh = [v,u'] \); \( \text{proc-of } \mathbf{fg} v = p \); \( \text{mon-c } \mathbf{fg} ch = \{\} \); \( !!s. s : \# \) \( ch \implies \exists p u v. s = [\text{entry fg } p] \land \\
(\langle u, \ell \text{Spawn } p, v \rangle) \in \text{edges } \mathbf{fg} \land \\
\ell \text{initialproc } \mathbf{fg} p \); \( !!P. (\lambda p. [\text{entry fg } p]) \cdot \# P \leq \# ch \implies \\
(v,\text{mon-w } \mathbf{fg} w,P) \in S-cs \mathbf{fg} (\text{size } P) \)

\( ] \implies Q \)

**Shows Q**

**Proof**

**Lemma (in flowgraph) S-precise-ntrp**:}
assumes \( \text{ENTRY}: (v, M, P) \in S\text{-cs fg} \) \( k \) \text{ and} \[ P: \text{proc-of fg } v = p \] \text{ and} \[ \text{EDGE}: (u, \text{Call } p, u') \in \text{edges fg} \]

shows \( \exists w \text{ ch.} \)
\[
((\{u\}, \{\#\}), \text{LOC } (\text{LCall } p \# w), ([v, u], \text{ch})) \in \text{entryp fg } \land \\
\text{size } P \leq k \land \\
M = \text{mon-w fg } w \land \\
\text{mon-n fg } v = \text{mon fg } p \land \\
(\lambda p. [\text{entry fg } p]) \cdot \# P \leq \# \text{ ch } \land \\
\text{mon-c fg } \text{ch} = \{\} 
\]

\[ \langle \text{proof} \rangle \]

9.2 Single reaching path

In this section we define a constraint system that collects abstract information of paths reaching a control node at \( U \). The path starts with a single initial thread. The collected information are the monitors used by the steps of the initial thread, the monitors used by steps of other threads and the acquisition history of the path. To distinguish the steps of the initial thread from steps of other threads, we use the loc/env-semantics (cf. Section 5.4).

9.2.1 Constraint system

An element \((u, M_l, M_e, h)\) \( \in R\text{-cs fg } U \) corresponds to a path from \( \{\#[u]\#\} \) to some configuration at \( U \), that uses monitors from \( M_l \) in the steps of the initial thread, monitors from \( M_e \) in the steps of other threads and has acquisition history \( h \).

Here, the correspondence between paths and entries included into the inductively defined set is not perfect but strong enough for our purposes: While each constraint system entry corresponds to a path, not each path corresponds to a constraint system entry. But for each path reaching a configuration at \( U \), we find an entry with less or equal monitors and an acquisition history less or equal to the acquisition history of the path.

\textbf{inductive-set}
\[
\text{RU-cs} :: (\text{'n}', \text{'p}', \text{'ba}', \text{'m}', \text{'more}}) \text{ flowgraph-rec-scheme } \Rightarrow \text{'n set } \Rightarrow \\
(\text{'n } \times \text{'m set } \times \text{'m set } \times (\text{'m } \Rightarrow \text{'m set})) \text{ set } \\
\text{for fg } U \\
\text{where} \\
\text{RU-init: } u \in U \implies (u, \{\}, \{\}, \lambda x.\{\}) \in R\text{-cs fg } U \\
\text{| RU-call: } [(u, \text{Call } p, u') \in \text{edges fg}; \text{proc-of fg } v = p; (v, M, P) \in S\text{-cs fg} \ 0; \\
(v, M_l, M_e, h) \in R\text{-cs fg } U; \text{ mon-n fg } u \cap M = \{\}] \implies (u, \text{mon fg } p \cup M \cup M_l, M_e, ah\text{-update } h (\text{mon fg } p, M) (M_l \cup M_e)) \in R\text{-cs fg } U \\
\text{| RU-spawn: } [(u, \text{Call } p, u') \in \text{edges fg}; \text{proc-of fg } v = p; (v, M, P) \in S\text{-cs fg} \ 1; \\
q ; \# P; (\text{entry fg } q, M_l, M_e, h) \in R\text{-cs fg } U; \\
(\text{mon-n fg } u \cup \text{mon fg } p) \cap (M_l \cup M_e) = \{\}] 
\]
The constraint system works by tracking only a single thread. Initially, there is just one thread, and from this thread we reach a configuration at $U$. After a macrostep, we have the transformed initial thread and some spawned threads. The key idea is, that the actual node $U$ is reached by just one of these threads. The steps of the other threads are useless for reaching $U$. Because of the nice properties of normalized paths, we can simply prune those steps from the path.

The $RU$-init-constraint reflects that we can reach a control node from itself with the empty path. The $RU$-call-constraint describes the case that $U$ is reached from the initial thread, and the $RU$-spawn-constraint describes the case that $U$ is reached from one of the spawned threads. In the two latter cases, we have to check whether prepending the macrostep to the reaching path is allowed or not due to monitor restrictions. In the call case, the procedure of the initial node must not own monitors that are used in the environment steps of the appended reaching path ($mon-n fg u \cap Me = \emptyset$). As we only test disjointness with the set of monitors used by the environment, reentrant monitors can be handled. In the spawn case, we have to check disjointness with both, the monitors of local and environment steps of the reaching path from the spawned thread, because from the perspective of the initial thread, all these steps are environment steps ($((mon-n fg u \cup mon fg p) \cap (Ml \cup Me)=\emptyset$). Note that in the call case, we do not need to explicitly check that the monitors used by the environment are disjoint from the monitors acquired by the called procedure because this already follows from the existence of a reaching path, as the starting point of this path already holds all these monitors.

However, in the spawn case, we have to check for both the monitors of the start node and of the called procedure to be compatible with the already known reaching path from the entry node of the spawned thread.

9.2.2 Soundness and precision

The following lemma intuitively states: If we can reach a configuration that is at $U$ from some start configuration, then there is a single thread in the start configuration that can reach a configuration at $U$ with a subword of the original path.

The proof follows from Lemma flowgraph.ntr-reverse-split-atU.

**lemma** (in flowgraph) ntr-reverse-split-atU:

**assumes** $V$: valid fg c and

$A$: atU $U$ c$'$ and

$B$: $(c,w,c') \in trcl (ntr fg)$

**shows** $\exists s w' c1'$.
The next lemma shows the soundness of the RU constraint system.

The proof works by induction over the length of the reaching path. For the empty path, the proposition follows by the RU-init-constraint. For a non-empty path, we consider the first step. It has transformed the initial thread and may have spawned some other threads. From the resulting configuration, $U$ is reached. Due to flowgraph.ntr-split we get two interleavable paths from the rest of the original path, one from the transformed initial thread and one from the spawned threads. We then distinguish two cases: if the first path reaches $U$, the proposition follows by the induction hypothesis and the RU-call constraint.

Otherwise, we use flowgraph.ntr-reverse-split-atU to identify the thread that actually reaches $U$ among all the spawned threads. Then we apply the induction hypothesis to the path of that thread and prepend the first step using the RU-spawn-constraint.

The main complexity of the proof script below results from fiddling with the monitors and converting between the multiset-and loc/env-semantics. Also the arguments to show that the acquisition histories are sound approximations require some space.

**Lemma (in flowgraph) RU-sound:**

$$ s;\#c \land w' \leq w \land c1' \leq \# c' \land atU U c1' \land ((\#s\#),w',c1') \in trcl (ntr fg) $$

⟨proof⟩

Now we prove a statement about the precision of the least solution. As in the precision proof of the $S$-cs constraint system, we construct a path for the entry on the conclusion side of each constraint, assuming that there already exists paths for the entries mentioned in the antecedent.

We show that each entry in the least solution corresponds exactly to some executable path, and is not just an under-approximation of a path: while for the soundness direction, we could only show that every executable path is under-approximated. The reason for this is that in effect, the constraint system prunes the steps of threads that are not needed to reach the control point. However, each pruned path is executable.

**Lemma (in flowgraph) RU-precise:**

$$ u s' c'. (([u],\{\#\}),w,(s',c')) \in trcl (ntrp fg); atU U \{{\#s'\#}\}+c'$$

oranginal = "s:#c∧w′≤w∧c1′≤# c′∧aatU U c1′∧({#:s#},w′,c1′)∈trcl (ntr fg)"

⟨proof⟩
where
mon-loc \( fg \) \( w = Ml \wedge \)
mon-env \( fg \) \( w = Me \wedge \)
\[ \alpha h (map (\text{and} \ fg) \ w) = h \]

9.3 Simultaneously reaching path

In this section, we define a constraint system that collects abstract information for paths starting at a single control node and reaching two program points simultaneously, one from a set \( U \) and one from a set \( V \).

9.3.1 Constraint system

An element \((u, Ml, Me) \in RUVC-sfg \ U \ V\) means, that there is a path from \{\#[u]\#\} to some configuration that is simultaneously at \( U \) and at \( V \). That path uses monitors from \( Ml \) in the first thread and monitors from \( Me \) in the other threads.

\textbf{inductive-set}
\begin{align*}
RUVC-sfg \ &\colon \ ('n, 'p, 'ba, 'm, 'more) \ \text{flowgraph-rec-scheme} \Rightarrow \\
&\ 'n\ \text{set} \Rightarrow \ 'n\ \text{set} \Rightarrow (n \times 'm\ \text{set} \times 'm\ \text{set})\ \text{set}
\end{align*}

\textbf{for} \( fg \ U \ V \)

\textbf{where}
\begin{align*}
RUVC-call: & \quad \[(u, Call\ p, u')\in edges\ fg;\ \text{proc-of}\ fg\ v = p; \ (v, M, P)\in S-scs\ fg\ 0; \\
&\ (v, Ml, Me)\in RUVC-sfg\ U\ V; \ \text{mon-n}\ fg\ u\ \cap\ Me = \{\} \]\ \Rightarrow\ (u, mon\ fg\ p\ \cup\ M\ \cup\ Ml, Me)\in RUVC-sfg\ U\ V
\end{align*}

\textbf{RUVC-split-le:}
\begin{align*}
&\quad \[(u, Call\ p, u')\in edges\ fg;\ \text{proc-of}\ fg\ v = p; \ (v, M, P)\in S-scs\ fg\ 1; \ q :\#\ P; \\
&\ (v, Ml, Me, h)\in RU-rcs\ fg\ U; \ (entry\ fg\ q, Ml', Me', h')\in RU-rcs\ fg\ V; \\
&\ (mon-n\ fg\ u\ \cup\ mon\ fg\ p)\ \cap\ (Me\cup Ml') = \{}; \ h [s] h'\]\ \Rightarrow\ (u, mon\ fg\ p\ \cup\ M\ \cup\ Ml, Me \cup Ml' \cup Me')\in RUVC-sfg\ U\ V
\end{align*}

\textbf{RUVC-split-el:}
\begin{align*}
&\quad \[(u, Call\ p, u')\in edges\ fg;\ \text{proc-of}\ fg\ v = p; \ (v, M, P)\in S-scs\ fg\ 1; \ q :\#\ P; \\
&\ (v, Ml, Me, h)\in RU-rcs\ fg\ U; \ (entry\ fg\ q, Ml', Me', h')\in RU-rcs\ fg\ U; \\
&\ (mon-n\ fg\ u\ \cup\ mon\ fg\ p)\ \cap\ (Me\cup Ml') = \{}; \ h [s] h'\]\ \Rightarrow\ (u, mon\ fg\ p\ \cup\ M\ \cup\ Ml, Me \cup Ml' \cup Me')\in RUVC-sfg\ U\ V
\end{align*}

\textbf{RUVC-split-ee:}
\begin{align*}
&\quad \[(u, Call\ p, u')\in edges\ fg;\ \text{proc-of}\ fg\ v = p; \ (v, M, P)\in S-scs\ fg\ 2; \\
&\ \{\#q\# + \{\#q\#\} \leq\#\ P; \\
&\ (entry\ fg\ q, Ml, Me, h)\in RU-rcs\ fg\ U; \ (entry\ fg\ q', Ml', Me', h')\in RU-rcs\ fg\ V; \\
&\ (mon-n\ fg\ u\ \cup\ mon\ fg\ p)\ \cap\ (Ml\cup Me\cup Ml'\cup Me') = \{}; \ h [s] h'\]\
$$\Rightarrow (u, \text{mon } p \cup M, \text{Me}) \in RUV\text{-cs } U V$$

The idea underlying this constraint system is similar to the \textit{RU-cs}-constraint system for reaching a single node set. Initially, we just track one thread. After a macrostep, we have a configuration consisting of the transformed initial thread and the spawned threads. From this configuration, we reach two nodes simultaneously, one in \textit{U} and one in \textit{V}. Each of these nodes is reached by just a single thread. The constraint system contains one constraint for each case how these threads are related to the initial and the spawned threads:

**RUV\_call** Both, \textit{U} and \textit{V} are reached from the initial thread.

**RUV\_spawn** Both, \textit{U} and \textit{V} are reached from a single spawned thread.

**RUV\_split\_le** \textit{U} is reached from the initial thread, \textit{V} is reached from a spawned thread.

**RUV\_split\_el** \textit{V} is reached from the initial thread, \textit{U} is reached from a spawned thread.

**RUV\_split\_ee** Both, \textit{U} and \textit{V} are reached from different spawned threads.

In the latter three cases, we have to analyze the interleaving of two paths each reaching a single control node. This is done via the acquisition history information that we collected in the \textit{RU-cs}-constraint system.

Note that we do not need an initializing constraint for the empty path, as a single configuration cannot simultaneously be at two control nodes.

### 9.3.2 Soundness and precision

**lemma (in flowgraph) RUV\_sound:** \textit{!!u s' c'}.

\[
\left( u, \text{mon } p \cup M, \text{Me} \right) \in RUV\text{-cs } U V \Rightarrow \exists Ml, Me. \\
\left( u, Ml, Me \right) \in RUV\text{-cs } U V \\
\text{atUV } U V (\{\#s'\#\} + c') \\
\text{mon-loc } f g w = Ml \\
\text{mon-env } f g w = Me
\]

— The soundness proof is done by induction over the length of the reaching path (proof)

**lemma (in flowgraph) RUV\_precise:** \textit{(u,Ml,Me) \in RUV\_cs } U V

\[
\Rightarrow \exists w s' c'. \\
\left( u, \{\#\}, w, (s', c') \right) \in \text{trcl} (\text{ntrp } f g) \\
\text{atUV } U V (\{\#s'\#\} + c') \\
\text{mon-loc } f g w = Ml \\
\text{mon-env } f g w = Me
\]

(proof)
10 Main Result

theory MainResult
imports ConstraintSystems
begin

At this point everything is available to prove the main result of this project: *The constraint system RUV-cs precisely characterizes simultaneously reachable control nodes w.r.t. to our semantic reference point.*

The „trusted base” of this proof, that are all definitions a reader that trusts the Isabelle prover must additionally trust, is the following:

- The flowgraph and the assumptions made on it in the flowgraph- and eflowgraph-locales. Note that we show in Section 6.4 that there is at least one non-trivial model of eflowgraph.
- The reference point semantics (refpoint) and the transitive closure operator (trcl).
- The definition of atUV.
- All dependencies of the above definitions in the Isabelle standard libraries.

\[
\text{theorem (in eflowgraph) RUV-is-sim-reach:}
(\exists w \ c'. (\{#|entry fg (main fg)|\#\}, w, c') \in \text{trcl (refpoint fg)} \land \text{atUV U V c'})
\iff
(\exists M_1 M_e. (entry fg (main fg), M_1, M_e) \in \text{RUV-cs fg U V})
\]
— The proof uses the soundness and precision theorems wrt. to normalized paths (flowgraph.RUV-sound, flowgraph.RUV-precise) as well as the normalization result, i.e. that every reachable configuration is also reachable using a normalized path (eflowgraph.normalize) and, vice versa, that every normalized path is also a usual path (ntr-is-tr). Finally the conversion between our working semantics and the semantic reference point is exploited (flowgraph.refpoint-eq).

end

11 Conclusion

We have formalized a flowgraph-based model for programs with recursive procedure calls, dynamic thread creation and reentrant monitors and its operational semantics. Based on the operational semantics, we defined a conflict as being able to simultaneously reach two control points from two given sets \( U \) and \( V \) when starting at the initial program configuration, just consisting of a single thread at the entry point of the main procedure. We then formalized a constraint-system-based analysis for conflicts and proved
it sound and precise w.r.t. the operational definition of a conflict. The main idea of the analysis was to restrict the possible schedules of a program. On the one hand, this restriction enabled the constraint system based analysis, on the other hand it did not change the set of reachable configurations (and thus the set of conflicts).

We characterized the constraint systems as inductive sets. While we did not derive an executable algorithm explicitly, the steps from the inductive sets characterization to an algorithm follow the path common in program analysis and pose no particular difficulty. The algorithm would have to construct a constraint system (system of inequalities over a finite height lattice) from a given program corresponding to the inductively defined sets studied here and then determine its least solution, e.g. by a worklist algorithm. In order to make the algorithm executable, we would have to introduce finiteness assumptions for our programs. The derivation of executable algorithms is currently in preparation.

A formal analysis of the algorithmic complexity of the problem will be presented elsewhere. Here we only present some results: Already the problem of deciding the reachability of a single control node is NP-hard, as can be shown by a simple reduction from SAT. On the other hand, we can decide simultaneous reachability in nondeterministic polynomial time in the program size, where the number of random bits depends on the possible nesting depth of the monitors. This can be shown by analyzing the constraint systems.

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References


